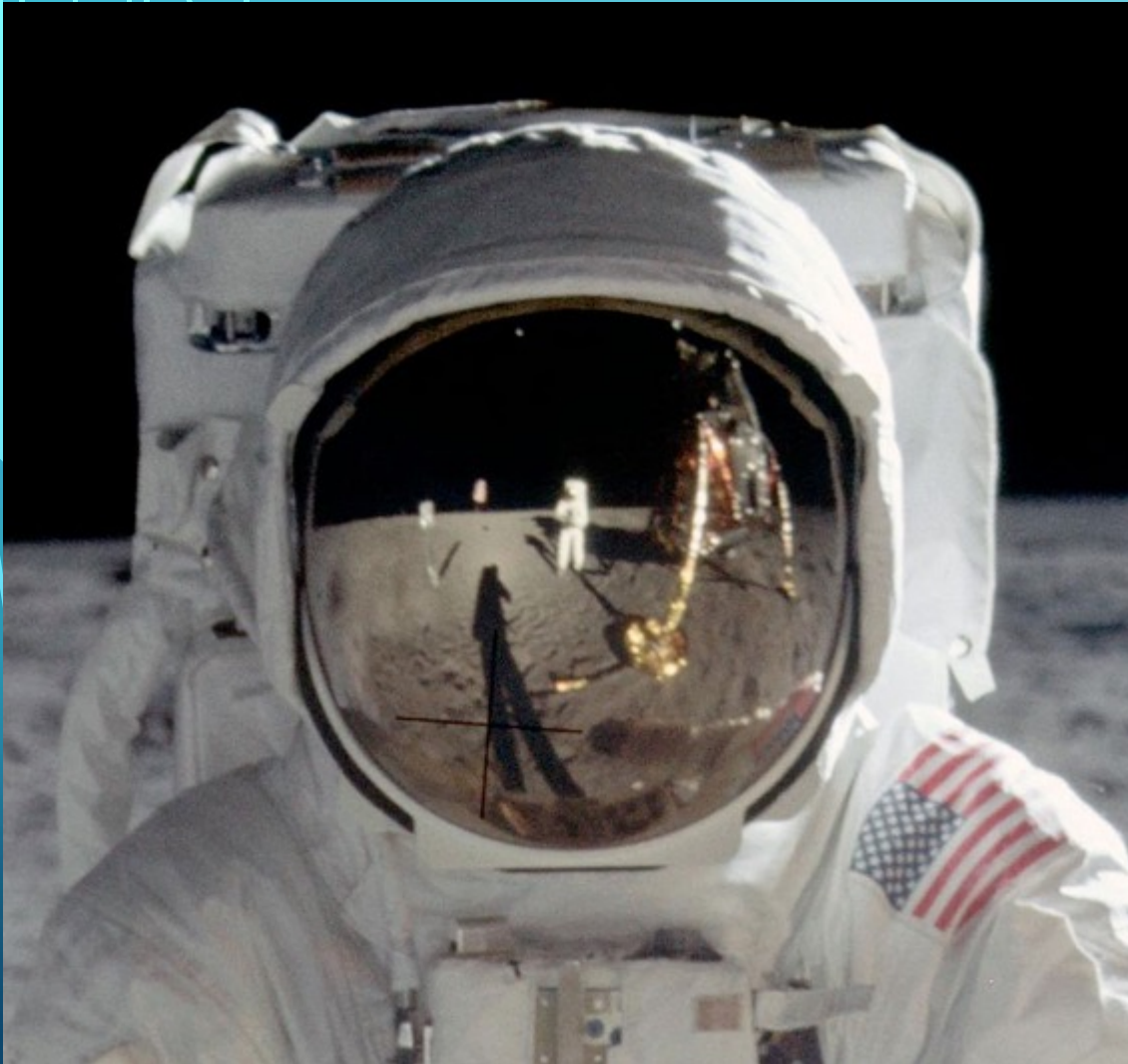


WHEN 100 FLOPS/WATT WAS A GIANT LEAP

THE APOLLO GUIDANCE COMPUTER
HARDWARE, SOFTWARE AND
APPLICATION IN MOON MISSIONS

Mark C Miller, LLNL (miller86@llnl.gov)

Presented at ATPESC
August 6th 2019

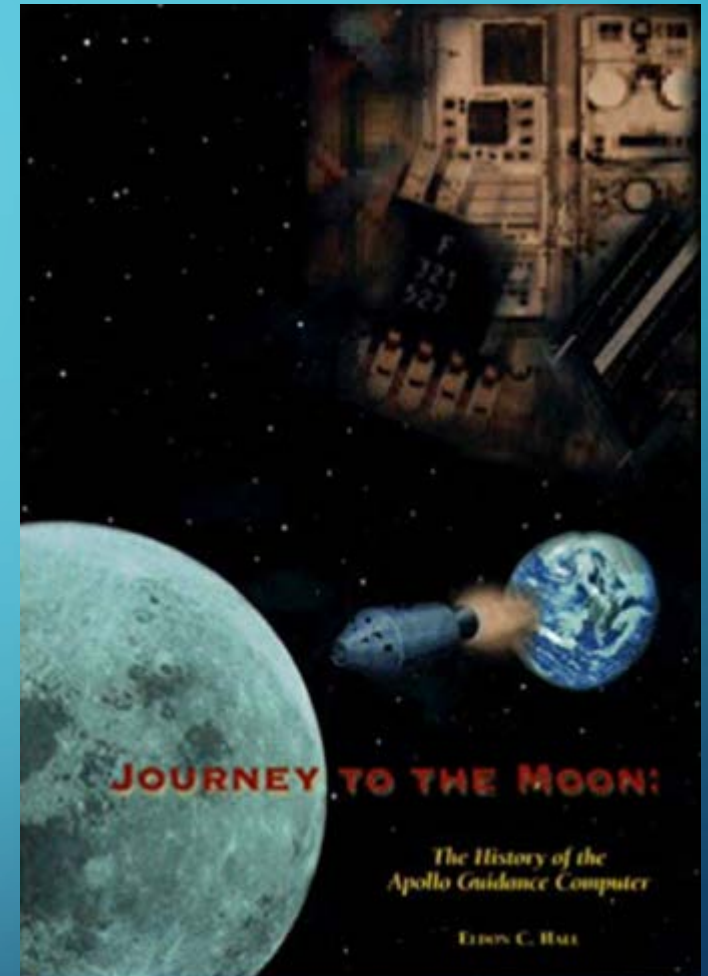
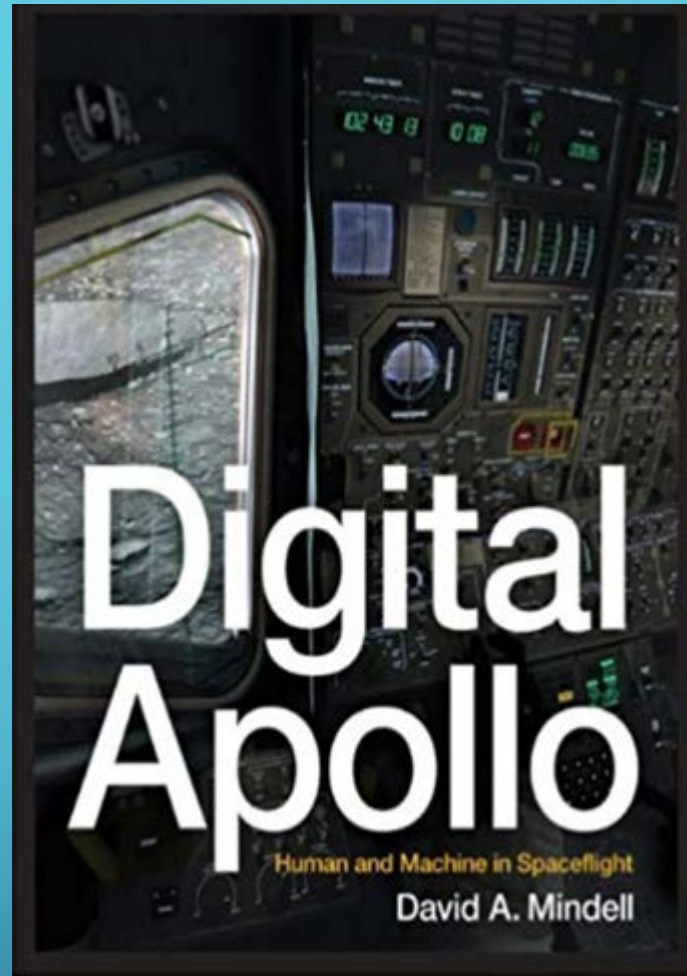
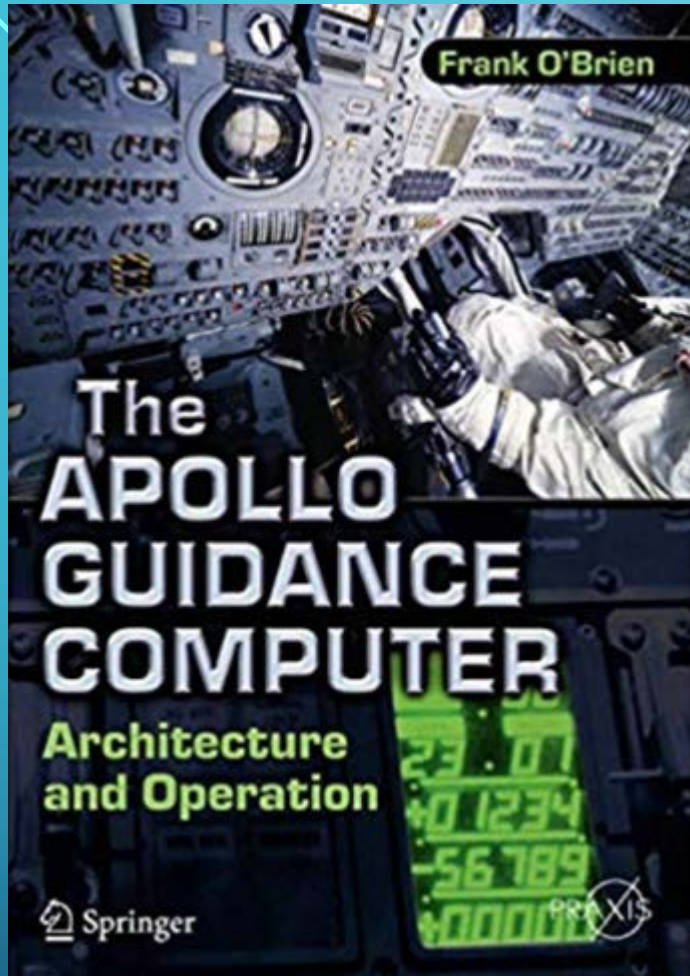


OUTLINE

- Background
- Hardware Architecture
- The Software Effort
- Brief Detour
- Mission Applications

CURRENT GENERATION HPC/CSE SOFTWARE DEVELOPERS WILL RECOGNIZE MANY COMMON THEMES

- Flops/Watt power constraints
- Checkpoint/Restart
- Performance Portability
- Co-Design
- Domain Specific Languages
- Role and impact of Non-Volatile Memory (NVM)



Virtual AGC Project: <https://www.ibiblio.org/apollo/>

3-Part Blog Series on Better Scientific Software Site (bssw.io)

[Part 1](#) | [Part 2](#) | [Part 3](#)

OUTLINE

- Background
- Hardware Architecture
- The Software Effort
- Brief Detour
- Mission Applications



WHAT WAS THE APOLLO PROGRAM?

- 10 year project, starting in 1961 to land people on the moon
- 7 Lunar Missions from Jul. 1969 – Dec. 1972
- The Apollo Guidance Computer (AGC) was instrumental in the success



Early Sixties State of the Art Computers

- 4,000 ft³
- 8 tons
- 125 Kilowatts
- MTBF \approx Days
- Reboot \geq 30 mins
- UI = Punch Cards & Printouts
- Time slice multi-tasking
- \sim 1 Flops/Watt

NASA RTCC 1964



Early Sixties State of the Art Computers

- 4,000 ft³
- 8 tons
- 125 Kilowatts
- MTBF \approx Days
- Reboot \geq 30 mins
- UI = Punch Cards & Printouts
- Time slice multi-tasking
- \sim 1 Flops/Watt

LLNL LARC1 1960 / B117

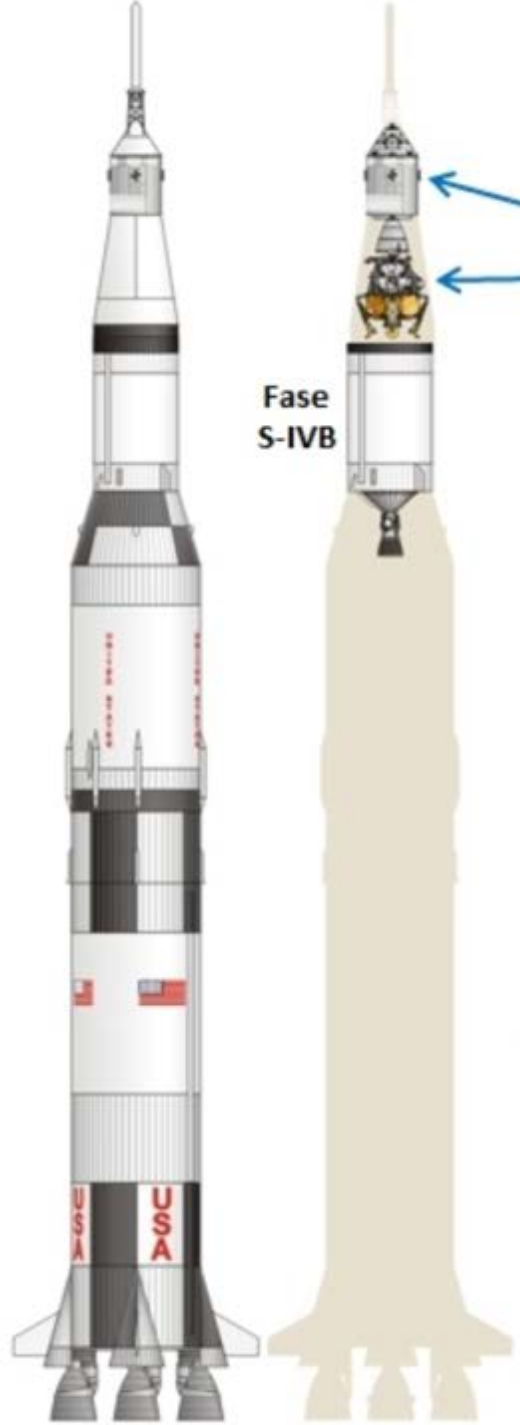


1966 BLOCK II AGC

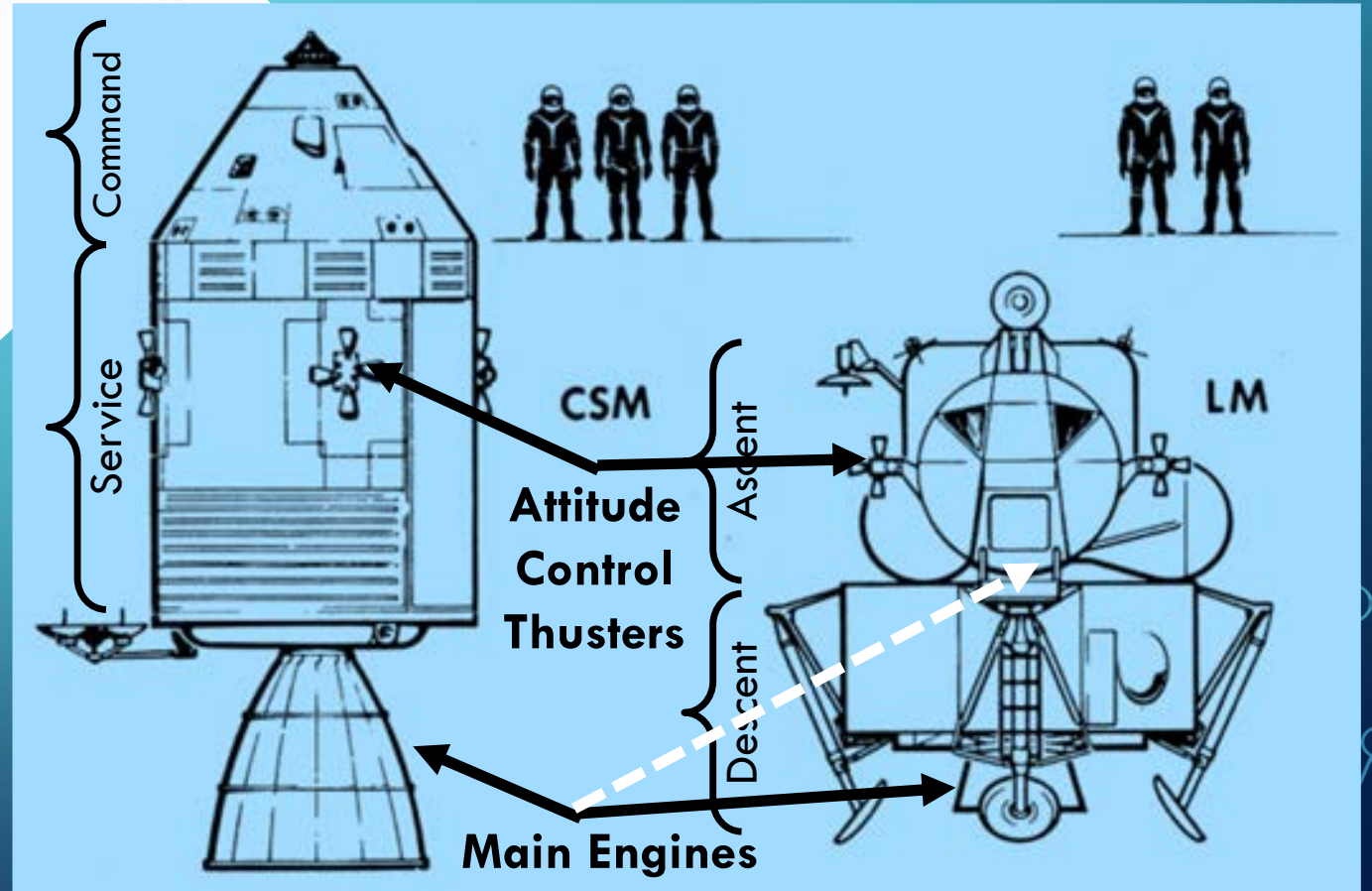


- 1 cubic foot volume
- 70 lbs weight
- 55 Watts power
- MTBF \geq Months
- Reboot \approx 7 seconds
- UI = Verb/Noun ELD (DSKY)
- Priority Based Multi-Tasking
- \sim 259 Flops/Watt

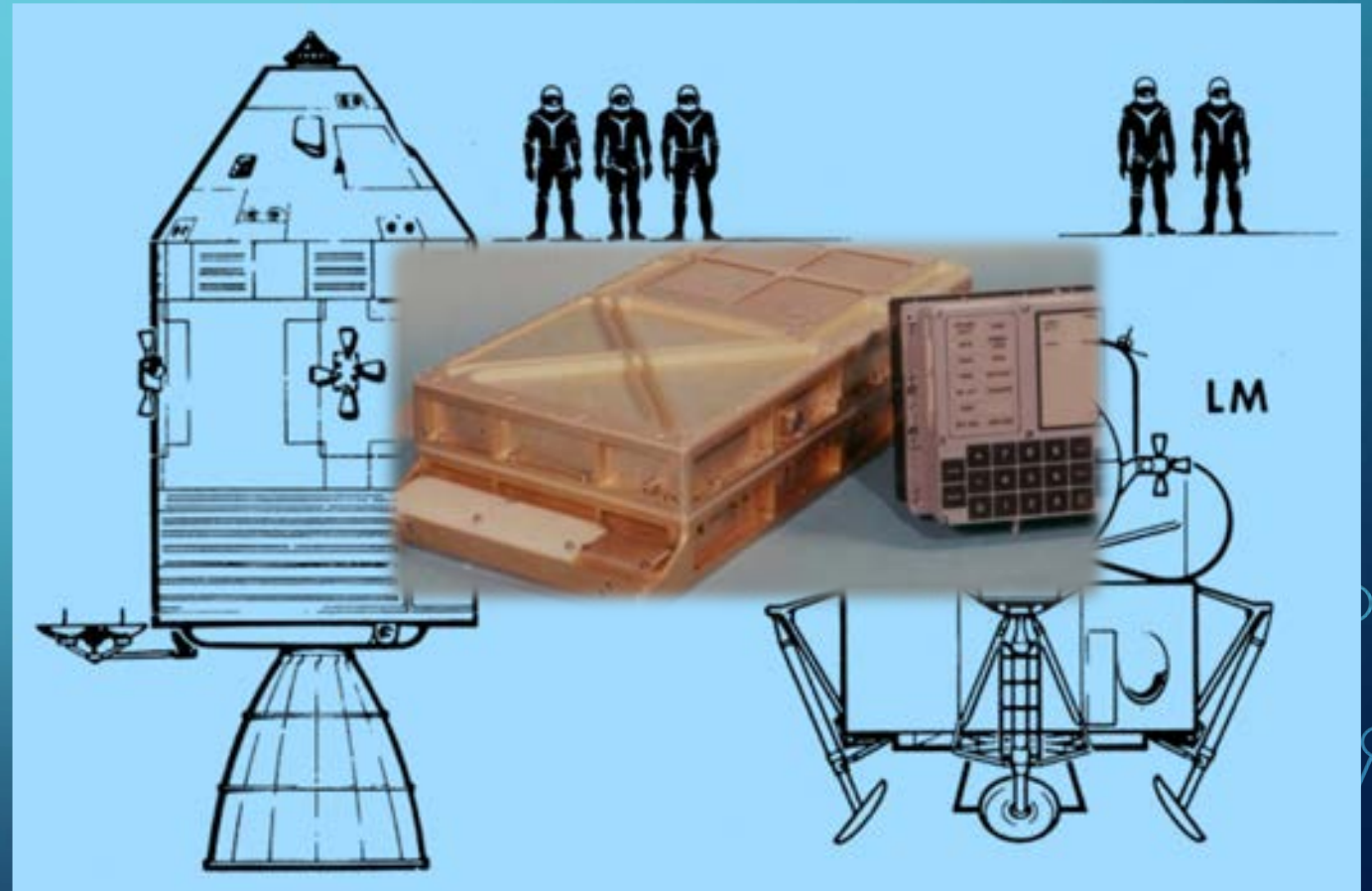
Apollo SPACECRAFT



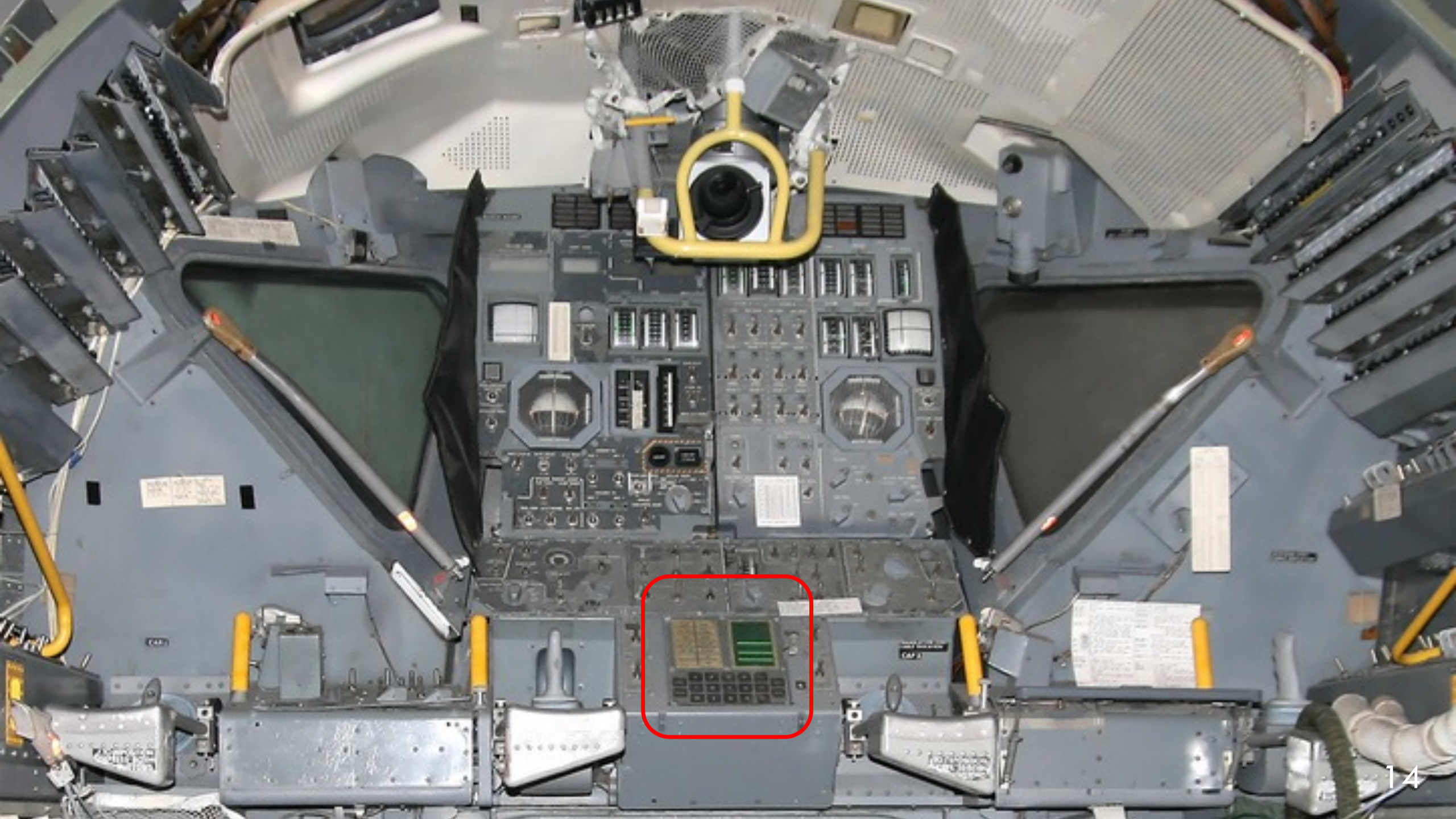
APOLO SPACECRAFT



APOLO SPACECRAFT





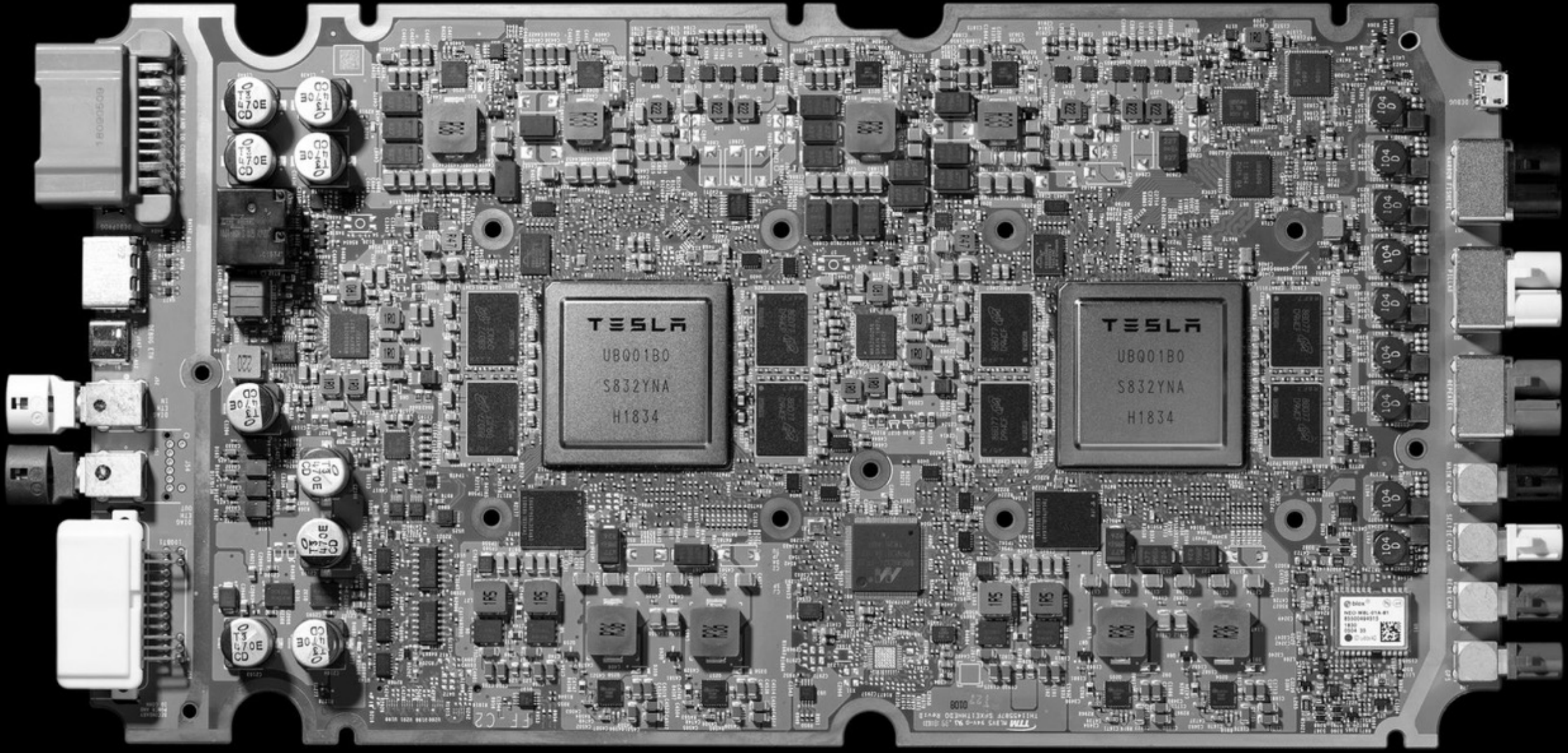


ROLE OF THE COMPUTER



ROLE OF THE COMPUTER

- In powered or coasting flight, manage the *State Vector*
 - Position & Position Rates
 - X, Y, Z & $\Delta X, \Delta Y, \Delta Z$
 - Attitude & Attitude Rates
 - $R(\text{oll}), P(\text{itch}), \gamma(\text{W})$ & $\Delta R, \Delta P, \Delta W$
- Real-time, Accurately, Reliably
- Autonomously
- In spite of many constraints and challenges...
 - Sensor noise, bias and drift
 - Avoiding orientations causing IMU gimbal lock
 - Moon's lumpy gravity field
 - Changing center of mass (fuel slosh & loss)
 - Minimize fuel consumption
 - Communication lapses and blackouts
 - Allowing for failures & contingencies



T+ 00:09:24

Peak acceleration @ 5G

LAUNCH: CRS-4 MISSION

06:01:41.28

SPACEX

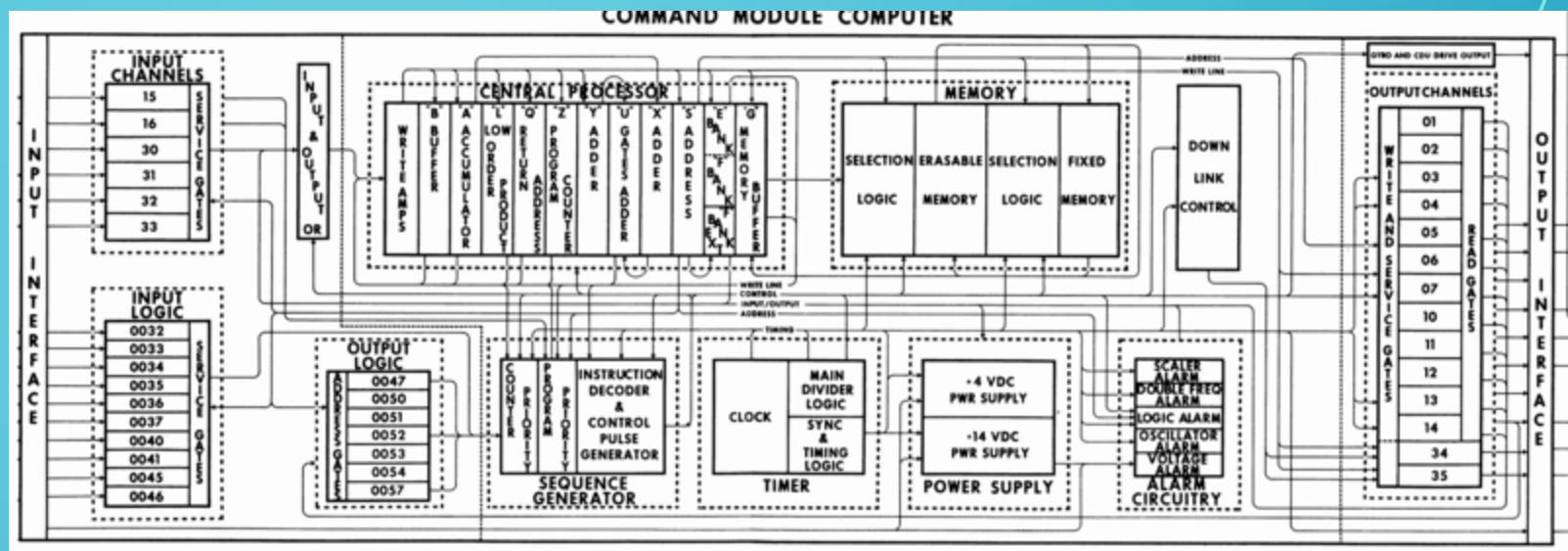
EXAMPLE MANEUVER: LUNAR ORBIT INSERTION (LOI)



- Velocity = 2 miles/sec
- Distance from moon = 60 miles
- RT signal to Earth = 2.5 sec
- Insertion burn on far side

OUTLINE

- Background
- Hardware Architecture
- Guidance Software
- Brief Detour
- Mission Applications



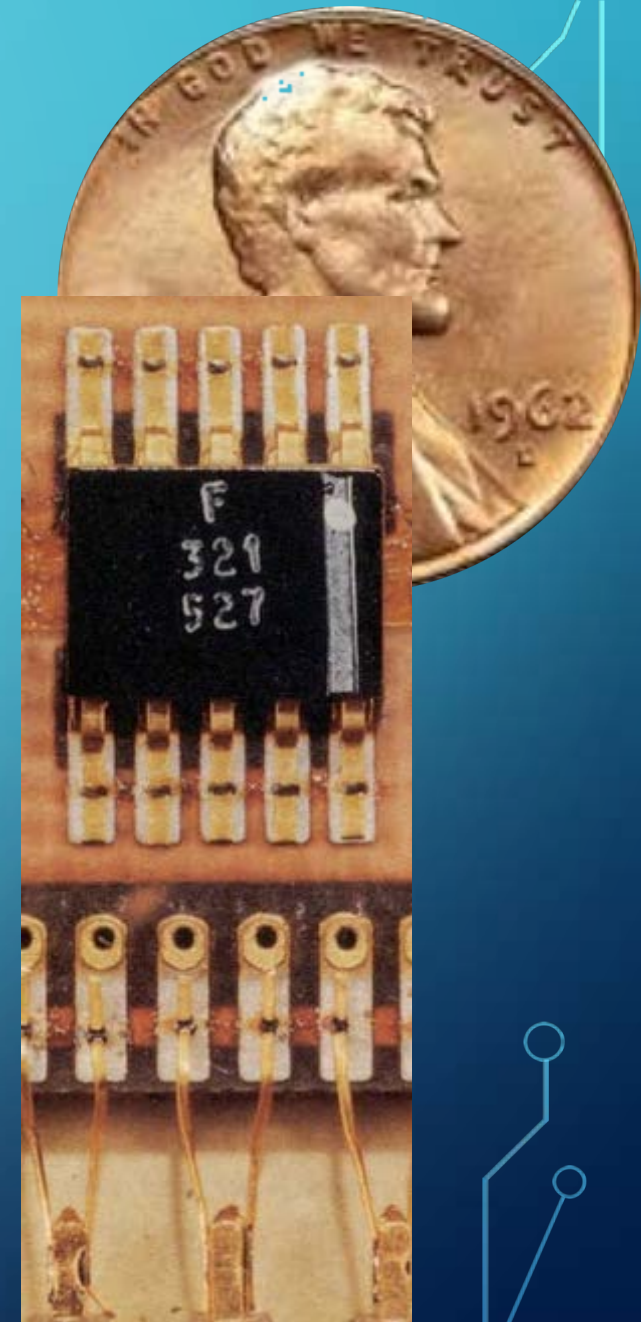
AGC HARDWARE OVERVIEW

SPECS

- 16 bit word size (15 + odd parity)
- 1.024 MHz Clock
- 12-pulse micro-seq'd instructions
- 4 central reg's + ~15 special reg's

TECHNOLOGY

- 2,800 ICs
- dual 3-input NOR



Please Send Invoices In
TRIPPLICATE
 to Massachusetts Institute
 of Technology Box 69
 Cambridge 39, Massachusetts

PURCHASE ORDER

Massachusetts Institute of Technology
 Cambridge 39, Massachusetts

PURCHASE ORDER NUMBER
IL120534
 This Number Must Appear on Invoices,
 B.L.S. Shipping Memos and Air Packages

PAGE 1 of 2

REQUISITION NUMBER

DATE **May 28, 1962**

ACCOUNT NUMBER
55-191-37-23

SHIP TO

TO **Fairchild Semi-Conductor Corp.**
36 North Road
Bedford, Massachusetts
ATTN: Mr. Bruce Giron

MASSACHUSETTS INSTITUTE OF TECHNOLOGY
 100 M. Tower
 77 MASSACHUSETTS AVENUE
66 Albany Street
 Cambridge 39, Mass.

E.J. Duggan W5-151

PLEASE FURNISH THE FOLLOWING MATERIALS OR SERVICES:

DATE REQUIRED	SHIP VIA	FOB	TERMS
SEE BELOW	AMS	SP	Net/30
ITEM QUANTITY	DESCRIPTION		PRICE

1	1000	pieces "B" Grade Gate Element in TO47 package, Fairchild P/N SL 1015 per MIT print C-88794 change B, @ \$31.10 each	31100.00
---	------	---	----------

①
④

1. To conform with MIT requirements, Fairchild P/N 1015 will have lead orientation in accordance with C-88794-B as agreed upon by Mr. E. Duggan of MIT, and R. Graham, M. Siegel, and B. Giron of Fairchild on May 15, 1962

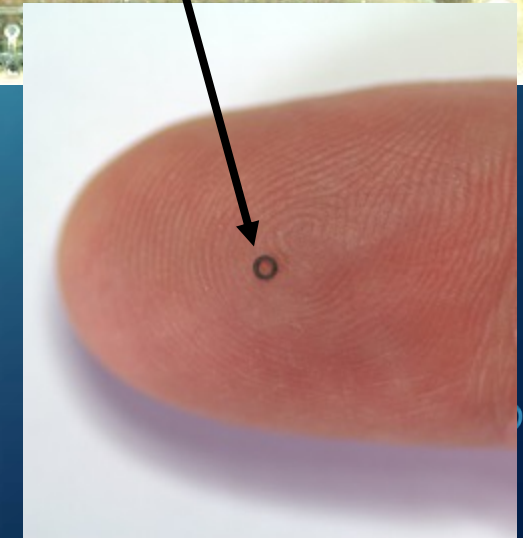
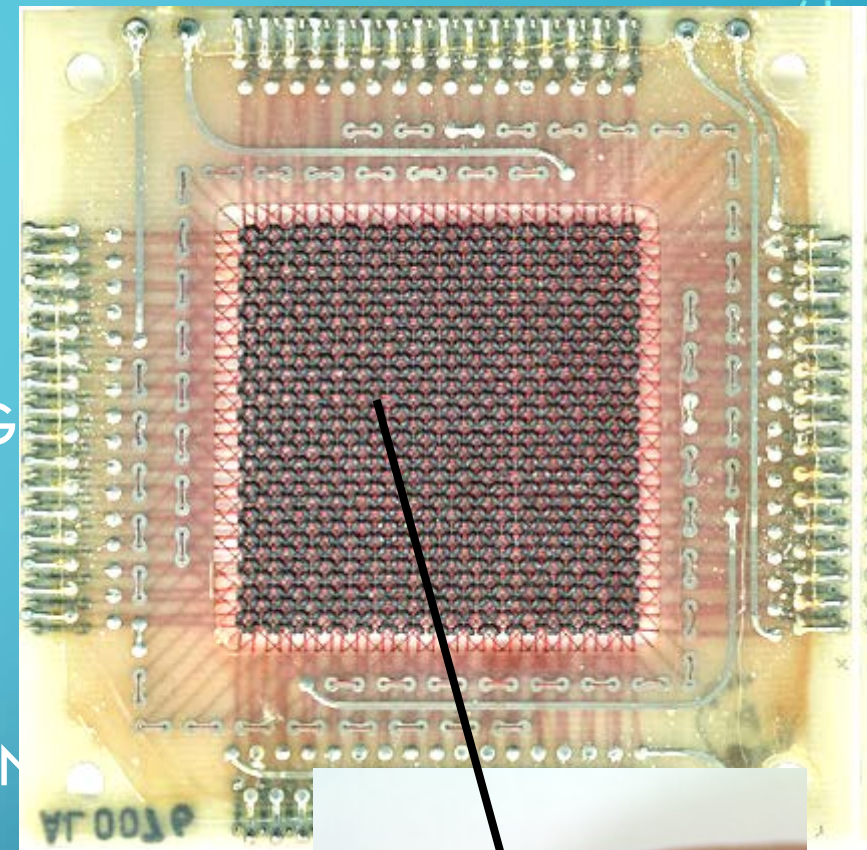
AGC HARDWARE OVERVIEW

SPECS

- 16 bit word size (15 + odd parity)
- 1.024 MHz Clock
- 12-pulse micro-seq'd instructions
- 4 central reg's + ~15 special reg's
- 2K words Erasable Memory (RAM)

TECHNOLOGY

- 2,800 ICs
- dual 3-input N
- Coincident-Current Core



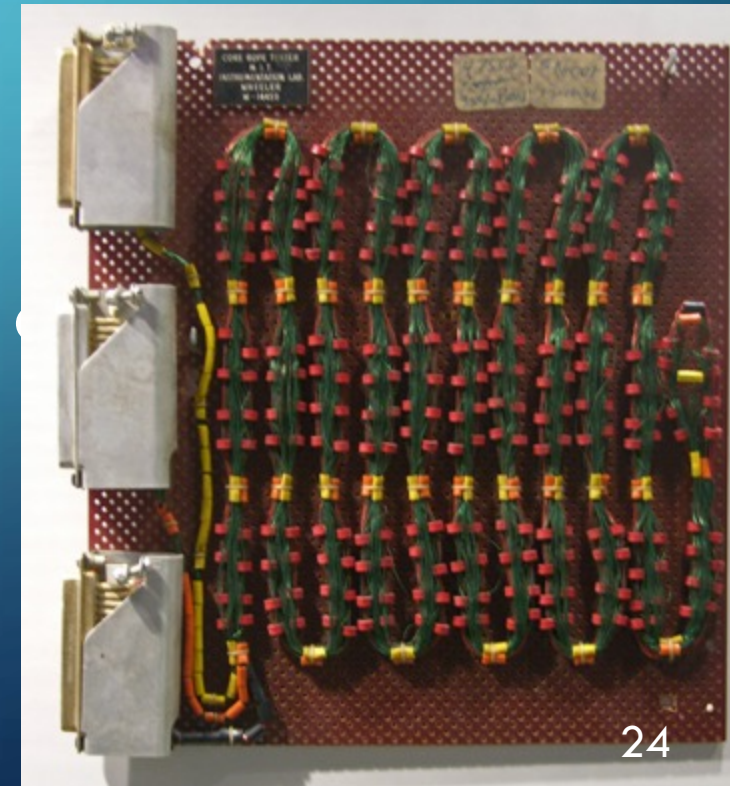
AGC HARDWARE OVERVIEW

SPECS

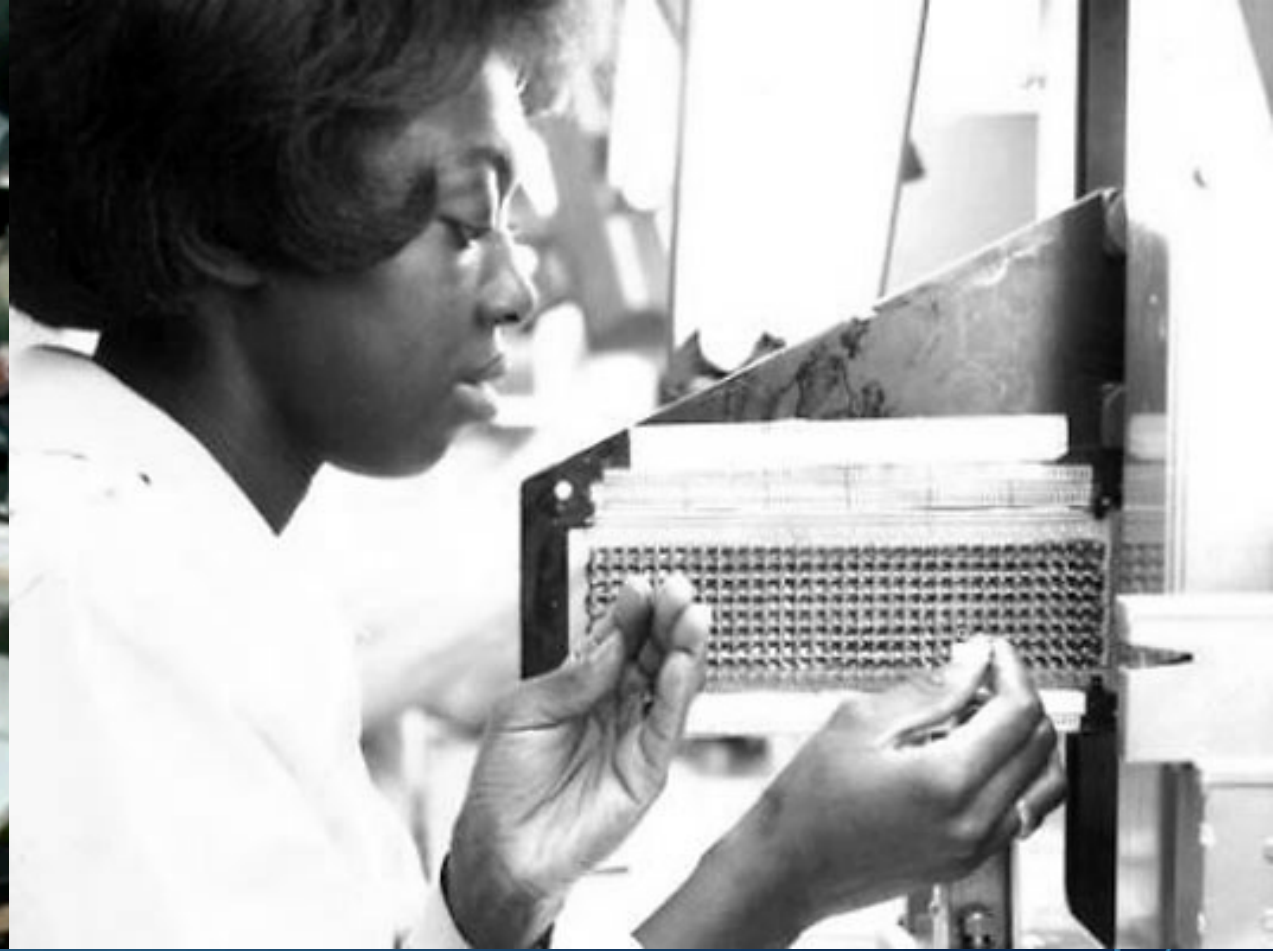
- 16 bit word size (15 + odd parity)
- 1.024 MHz Clock
- 12-pulse micro-seq'd instructions
- 4 central reg's + ~15 special reg's
- 2K words Erasable Memory (RAM)
- 36K words Fixed Memory (ROM)
- **Both RAM and ROM were NVM**

TECHNOLOGY

- 2,800 ICs
- dual 3-input NOR
- Coincident-Current
- Woven Rope Core

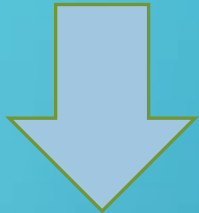


AGC HARDWARE OVERVIEW





CPU



Fixed / Erasable Memory
Timing, I/O

AGC ARCHITECTURE OVERVIEW

- 4 Central registers
 - A: accumulator w/overflow bit
 - Z: program counter
 - Q: div-remainder / return address
 - L: lower-product
- Other special purposes registers
 - ROM / RAM memory banking
 - Editing (shift) registers
 - Zero / NEWJOB (00067₈)
 - Not directly programmable
- 8 basic + 33 extended instructions

EXAMPLE CODE SNIPIT

line	label	opcode	address	comments
0184	P63SPOT3	CA	BIT6	IS THE LR ANTENNA IN POSITION 1 YET
0185		EXTEND		
0186		RAND	CHAN33	
0187		EXTEND		
0188		BZF	P63SPOT4	BRANCH IF ANTENNA ALREADY IN POSITION 1
0189		CAF	CODE500	ASTRONAUT: PLEASE CRANK THE
0190		TC	BANKCALL	SILLY THING AROUND
0191		CADR	GOPERF1	
0192		TCF	GOTOP00H	TERMINATE
0193		TCF	P63SPOT3	PROCEED SEE IF HE'S LYING
0194	P63SPOT4	TC	BANKCALL	ENTER INITIALIZE LANDING RADAR
0195		CADR	SETPOS1	
0196		TC	POSTJUMP	OFF TO SEE THE WIZARD...
0197		CADR	BURNBABY	

THE INTERPRETER: A SPACE GUIDANCE DSL (A P-CODE MACHINE LIKE JVM)

VIRTUAL REGISTER SET

- MPAC (virtual accumulator)
- OVFLND (virtual overflow bit)
- ADRLOC (virtual program counter)
- QPRET (virtual Q/L)
- X1, X2, S1, S2 (index, temp)
- PUSHLOC/PUSHLIST (small stack)

VIRTUAL INSTRUCTION SET

- S|D|T|VLOAD & S|D|T|VSTORE
- S|D|T|VAD & S|D|T|VSU
- SQRT
- DOT
- NORM
- SIN/COS/ASIN/ACOS

A form of compression to tradeoff memory space for time

Problem: Compute $\underline{z} = aM(\underline{x} + \underline{y})$
 where a is a scalar and M a 3×3 matrix

Program (requires 7 words of storage)

Explanation

VXSC	MXV	} Operation Codes
	X	} Operand Adresses
	Y	
	M	
	A	
STORE	Z	} Left-over address used to store result

- 1) The first address of an equation is used to load an accumulator; VAD requests a vector load.
- 2) Each op code results in a subroutine call with the corresponding address left in a standard location.
- 3) After all op codes have been "executed," the remaining address is used to store the result. Since the result of the last operation is a vector, a vector will be stored in Z.

NUMERICS OF THE AGC

WHOLE NUMBERS

- 16 bit, 1's compliment, big endian
- 1 bit for parity (bit 16) & sign (bit 15)
- 14 bits for magnitude range $0 \dots 2^{14} - 1$
- Full range of $-16,383_{10}$ to $+16,383_{10}$

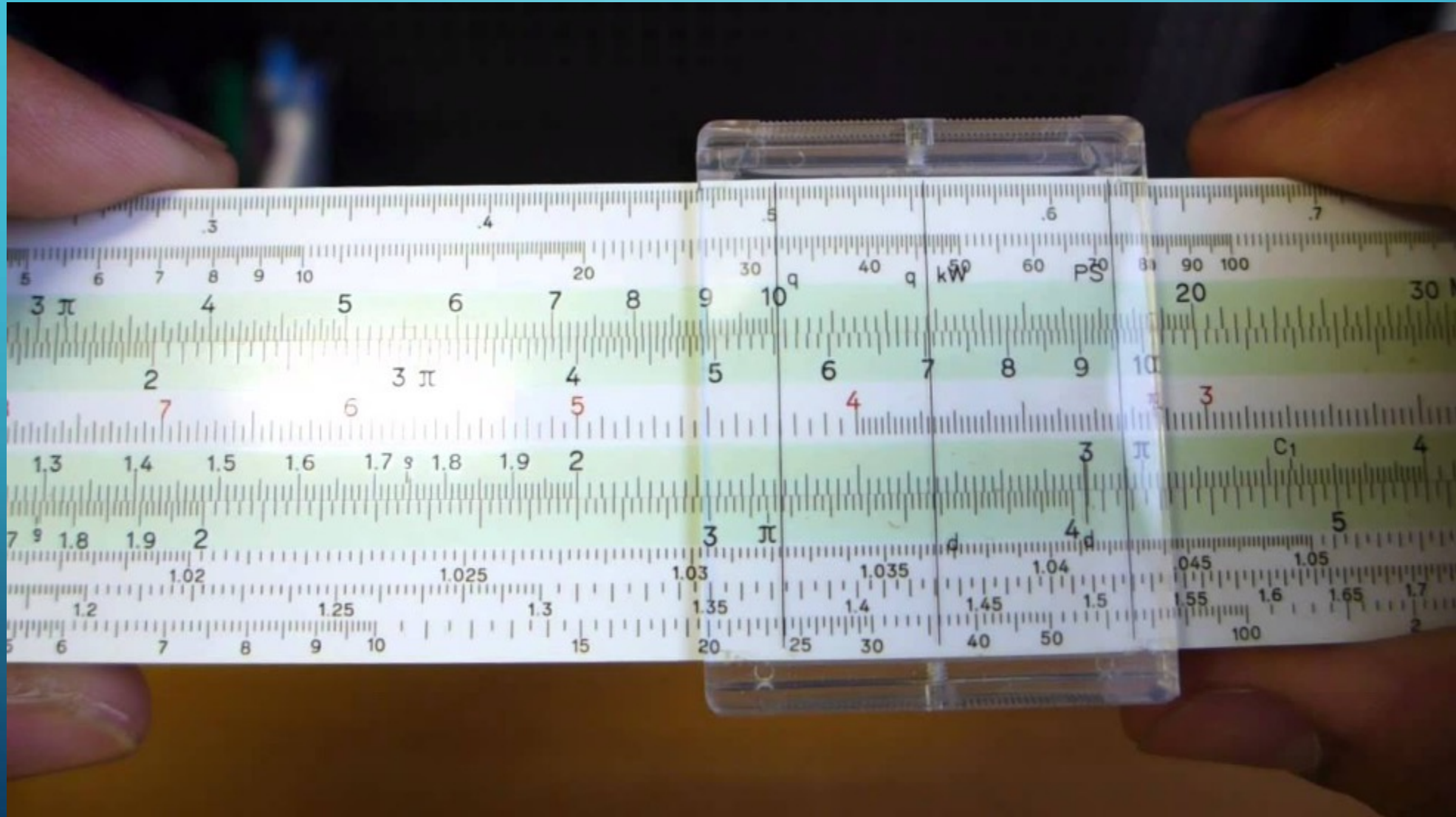
P | S | 2^{13} | 2^{12} | 2^{11} | ... | 2^1 | 2^0 |

FRACTIONAL NUMBERS

P | S | 2^{-1} | 2^{-2} | 2^{-3} | ... | 2^{-13} | 2^{-14} |

- Fixed Point Representation
- Coders must ensure proper scaling!!
- Just like an Engineer's Slide Rule

NUMERICS OF THE AGC



NUMERICS OF THE AGC

WHOLE NUMBERS

- 16 bit, 1's compliment, big endian
- 1 bit for parity (bit 16) & sign (bit 15)
- 14 bits for magnitude range $0 \dots 2^{14}-1$
- Full range of $-16,383_{10}$ to $+16,383_{10}$

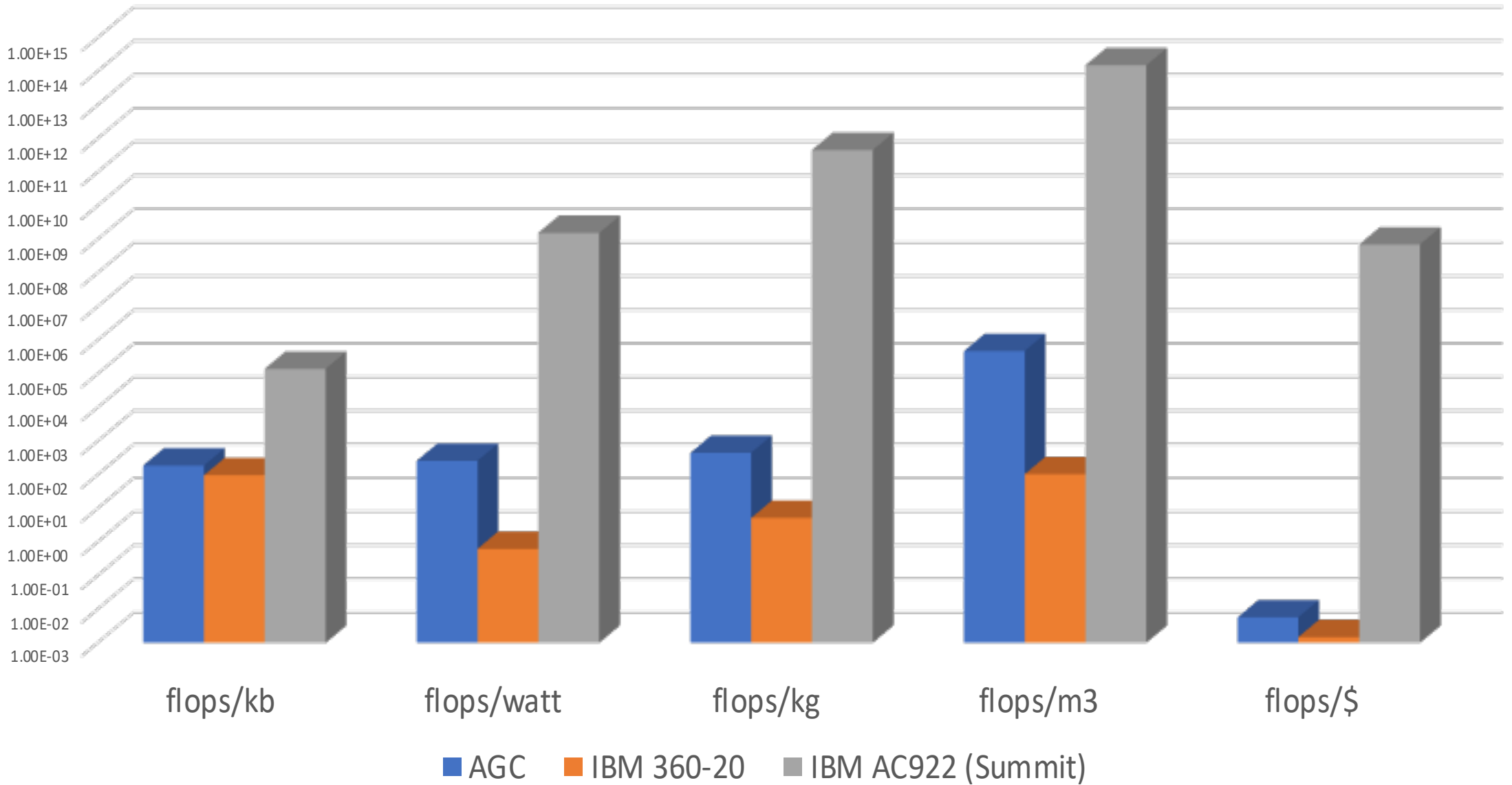
P | S | 2^{13} | 2^{12} | 2^{11} | ... | 2^1 | 2^0 |

FRACTIONAL NUMBERS

P | S | 2^{-1} | 2^{-2} | 2^{-3} | ... | 2^{-13} | 2^{-14} |

- Fixed Point Representation
- Coders must ensure proper scaling!!
- Just like an Engineer's Slide Rule
- Single, **double**, triple precision
- **$+, \times, \div = 35.1, 70.2, 133.4 \mu\text{sec}$**

Flops/x Computing Metrics Comparison



THE AGC EXECUTIVE (PROCESS MANAGER)

"TASKS"

- Short, finely tuned
 - < 5 ms (150-200 instructions)
- Scheduled by time (in the future)
- Some tasks only schedule a "job"

"JOBS"

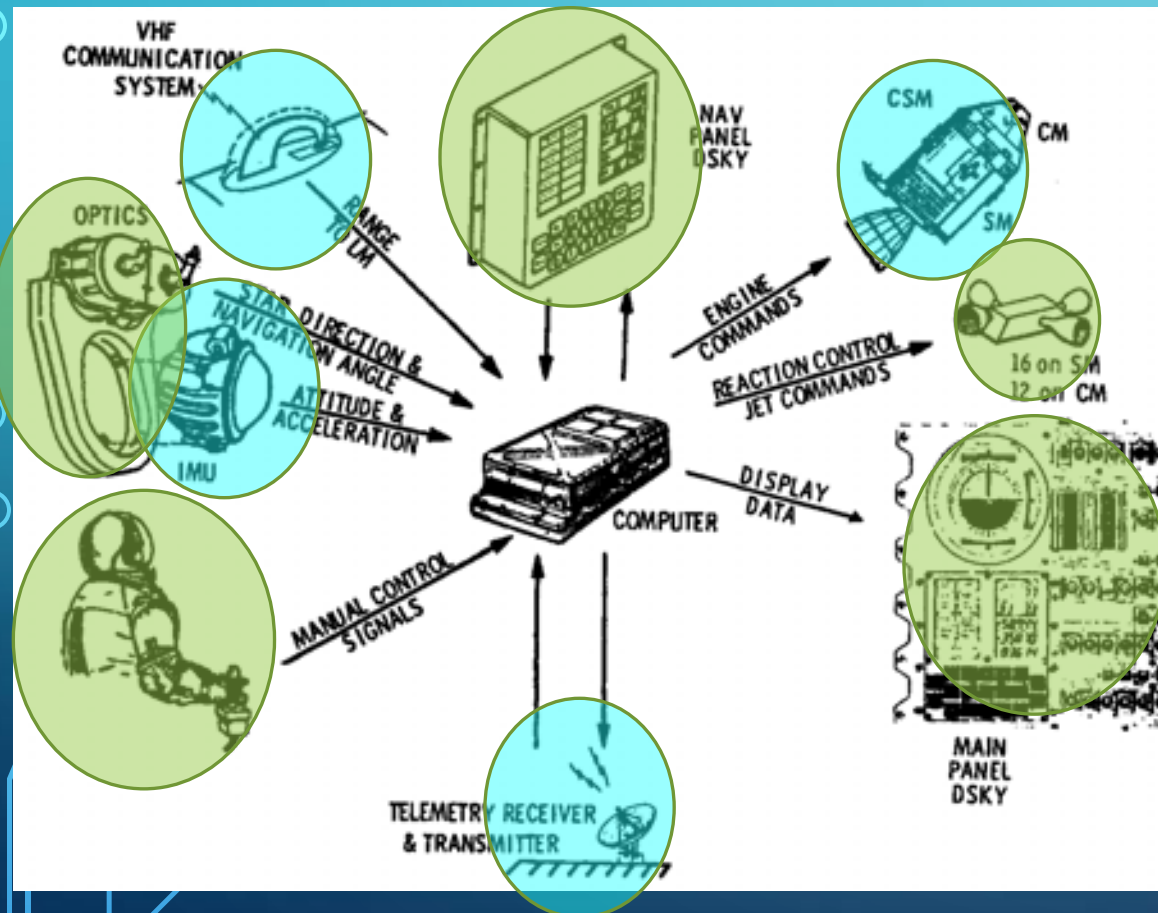
- Priority Scheduled
- 12 words of state (4 regs + other)
- Jobs adjust own priority up/down

WAYPOINT AND RESTART

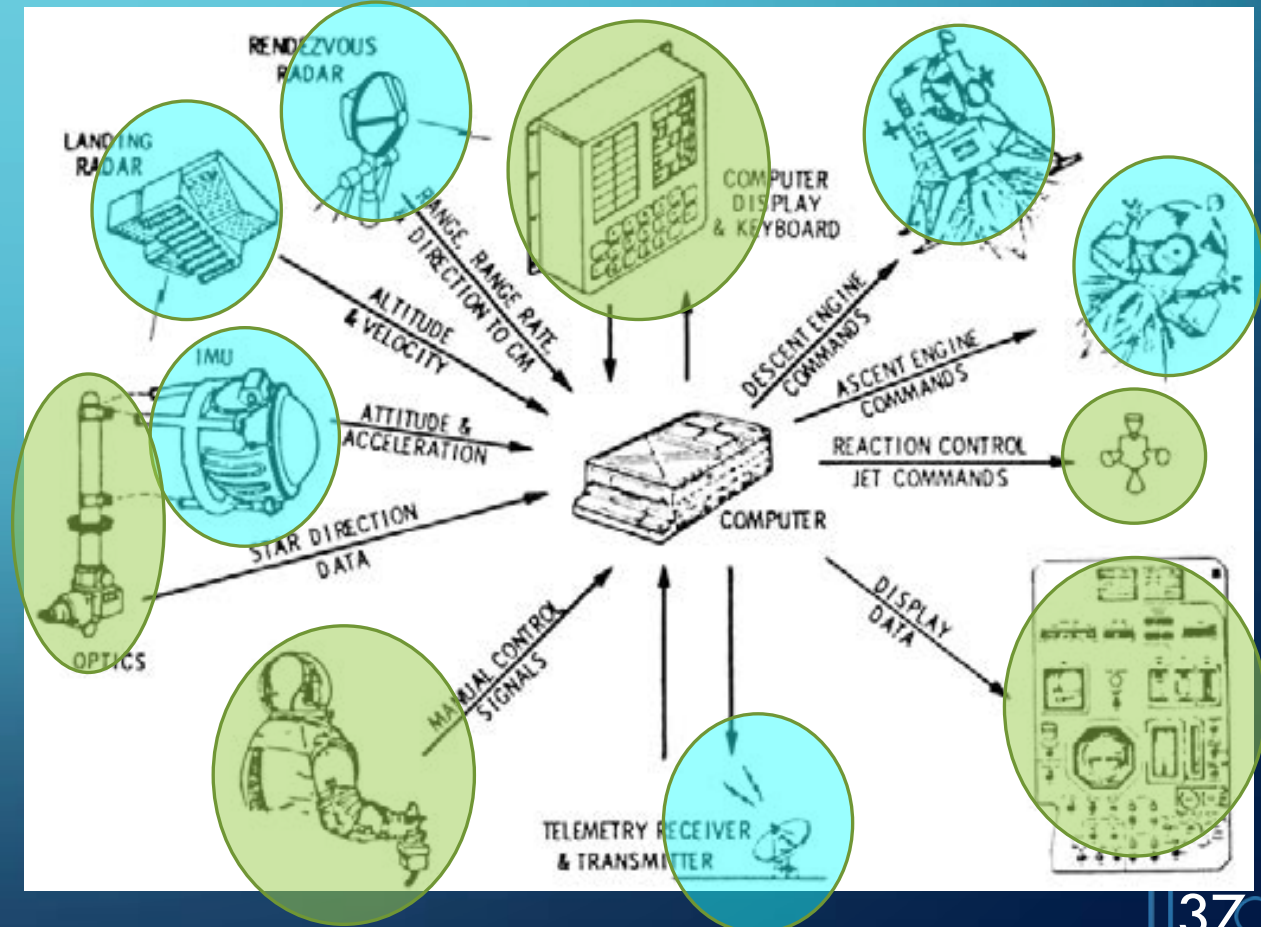
- Critical routines were restart protected
- Waypoints periodically updated in erasable memory
- Consumed 4% of fixed memory, additional coding and testing complexity

I/O DEVICES

Command Module



Lunar Module



FAULT TOLERANCE INCORPORATED AT MANY LEVELS

- Power system glitch detection
- Parity check every memory ref
- Job hog/freeze detection (NEWJOB night watchman)
- Interrupt lockout detection
- Program Alarms (e.g. Exceptions)
- System Restarts (< 7 seconds)
- System self-checks
- MTBF → 40,000 hours (due to quality ICs)
- Modern systems using triple redundant hardware & voting

OUTLINE

- Background
- Hardware Architecture
- Guidance Software
- Brief Detour
- Mission Applications



CLASS OF SERVICE
This is a fast message unless its deferred character is indicated by the proper symbol.

WESTERN UNION

TELEGRAM

SYMBOLS
DL = Day Letter
NL = Night Letter
LT = International Letter Telegram

W. P. MARSHALL, PRESIDENT

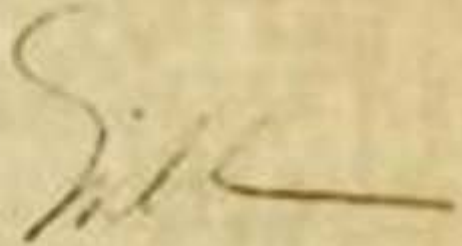
BF-1201 (4-60)

The filing time shown in the destination of telegrams is LOCAL TIME at point of destination.

435P EDT AUG 9 61 BB257 PB375
W NFA084 GOVT PD NF WASHINGTON DC 9 405P EDT
DR STARK DRAPER, DIR

INSTRUMENTAL LABORATORY MASSACHUSETTS INST OF TECHNOLOGY
CAMBRIDGE MASS

MIT - A N - 4415



PLEASED TO ADVISE THAT THE NATIONAL AERONAUTICS AND SPACE ADMINISTRATI
ON TODAY ANNOUNCED THAT MIT'S INSTRUMENTATION LABORATORY HAS
BEEN SELECTED TO DEVELOP THE GUIDANCE NAVIGATION SYSTEM OF THE
PROJECT APOLLO SPACECRAFT. APOLLO IS CAPABLE OF CARRYING THREE
MEN TO THE MOON AND BACK. MIT IS THE FIRST MEMBER OF THE APOLLO
TEAM TO BE CHOSEN. BIDS ARE NOW UNDERWAY FOR THE PRIME CONTRACTOR'S
JOB. IN ADDITION TO APOLLO THE INSTRUMENTATION LABORATORY WILL
ALSO DEVELOP THE GROUND SUPPORT AND CHECKOUT EQUIPMENT. CONTRACT
COVERING THE FIRST YEAR IS AN ESTIMATED \$4 MILLION
LEVERETT SALTONSTALL UNITED STATES SENATOR.

EXTREME CO-DESIGN

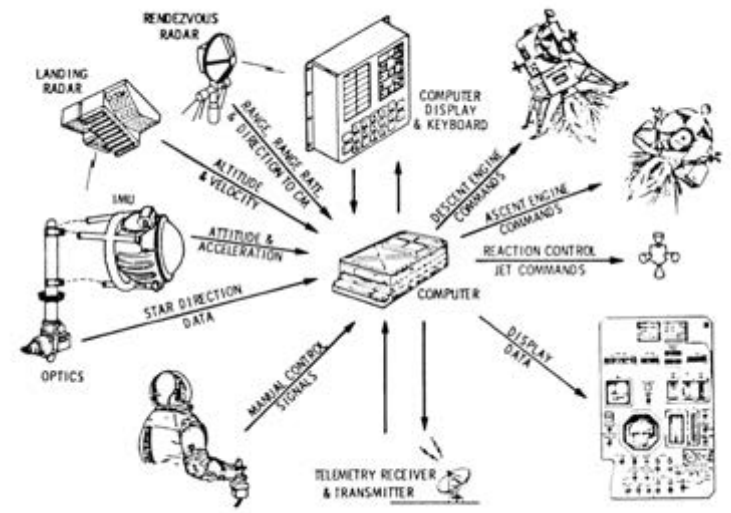
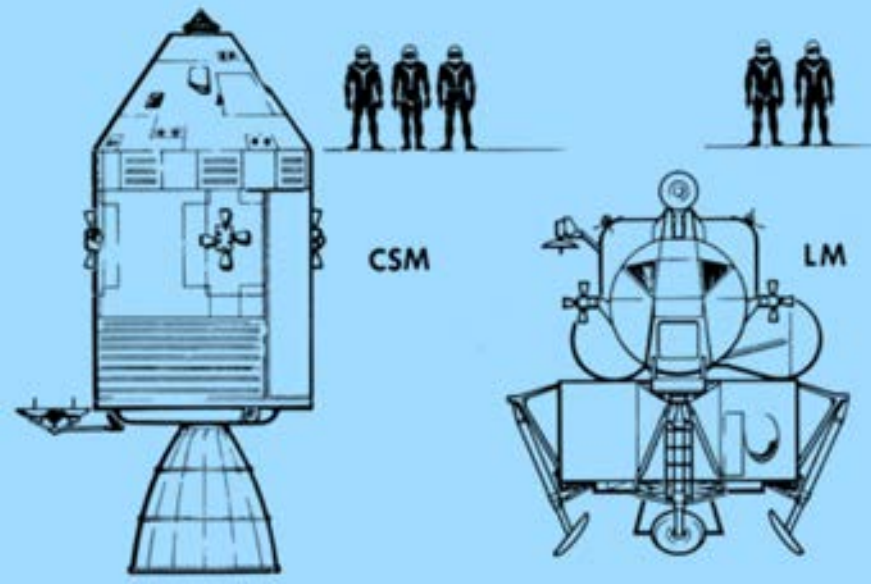
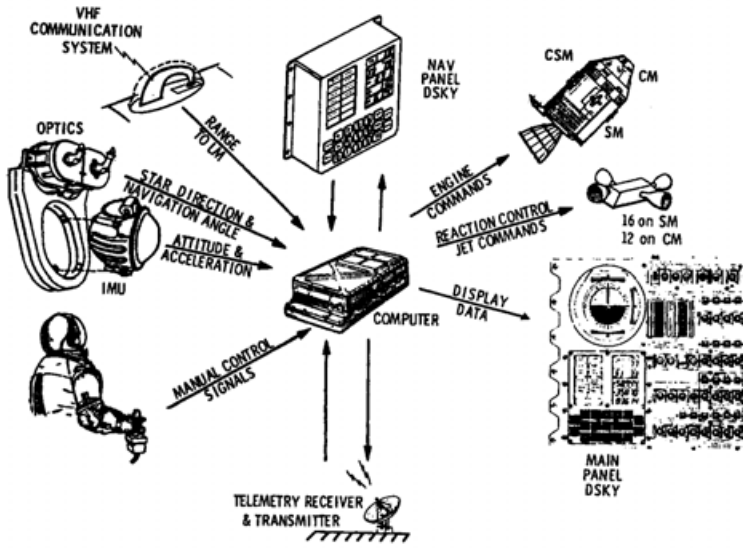
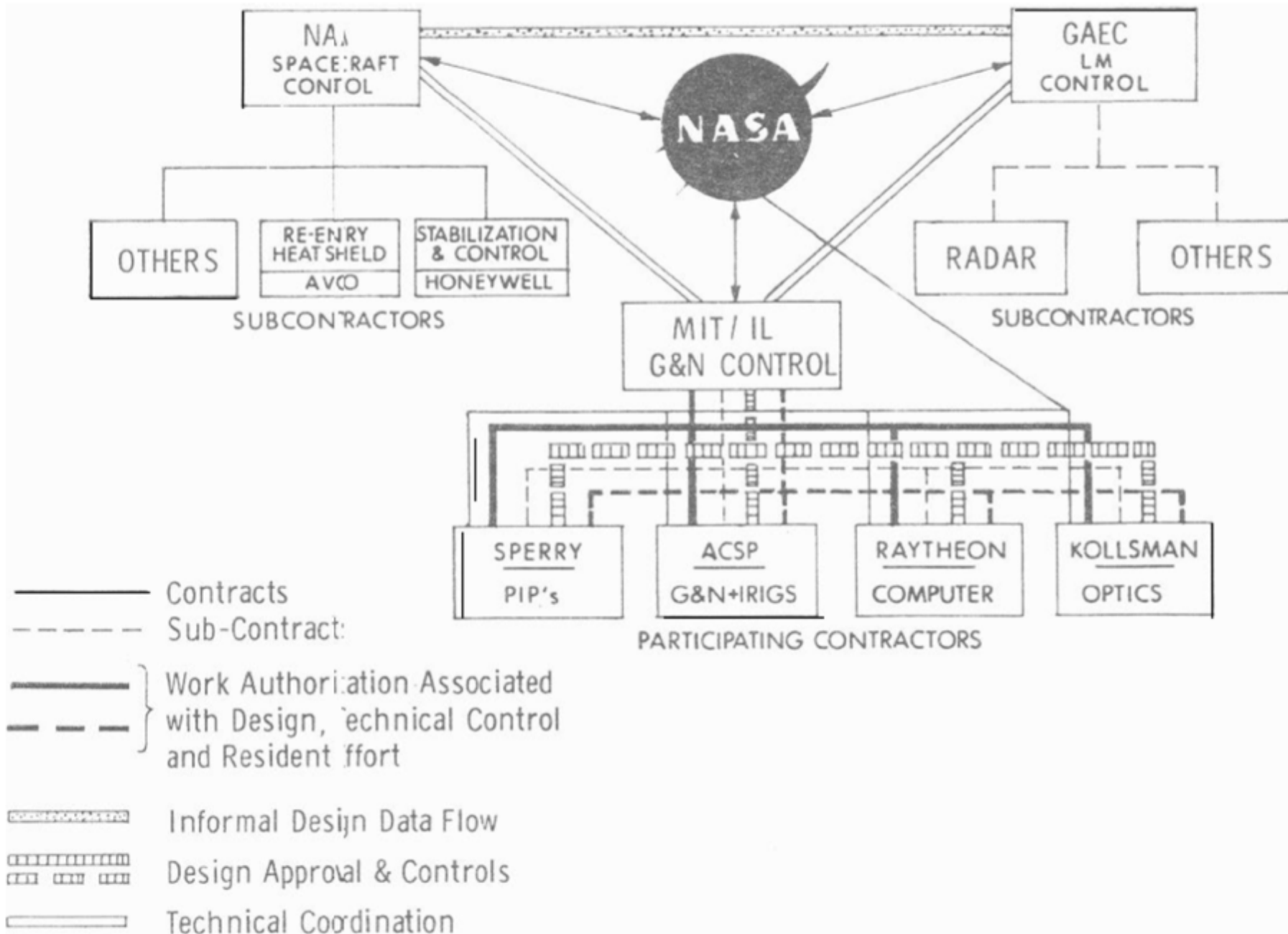


Fig. 1 The Lunar Module Computer Interfaces

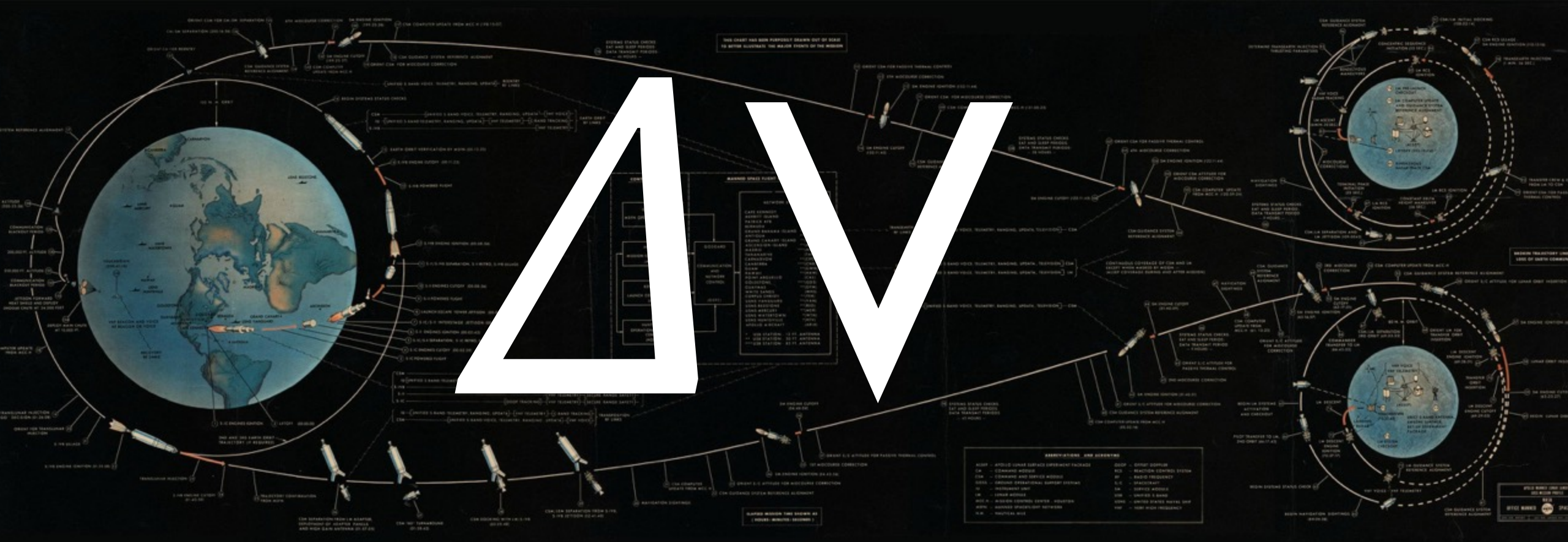
- None of these components were known when MIT was awarded the PGNCS contract
- NASA didn't decide upon Lunar Orbit Rendezvous (LOR) until a year later
- Everything was being developed essentially simultaneously



THE ESSENTIAL STEP MIT SOFTWARE ENGINEERS NEEDED TO PERFORM

- Assemble a “flight program” & release it to Raytheon for rope core weaving
 - 2 months to weave the ropes
 - 2 months to install, test, run crew rehearsals, etc.
- For ~30 planned flights (uncrewed and crewed), some with unique guidance requirements

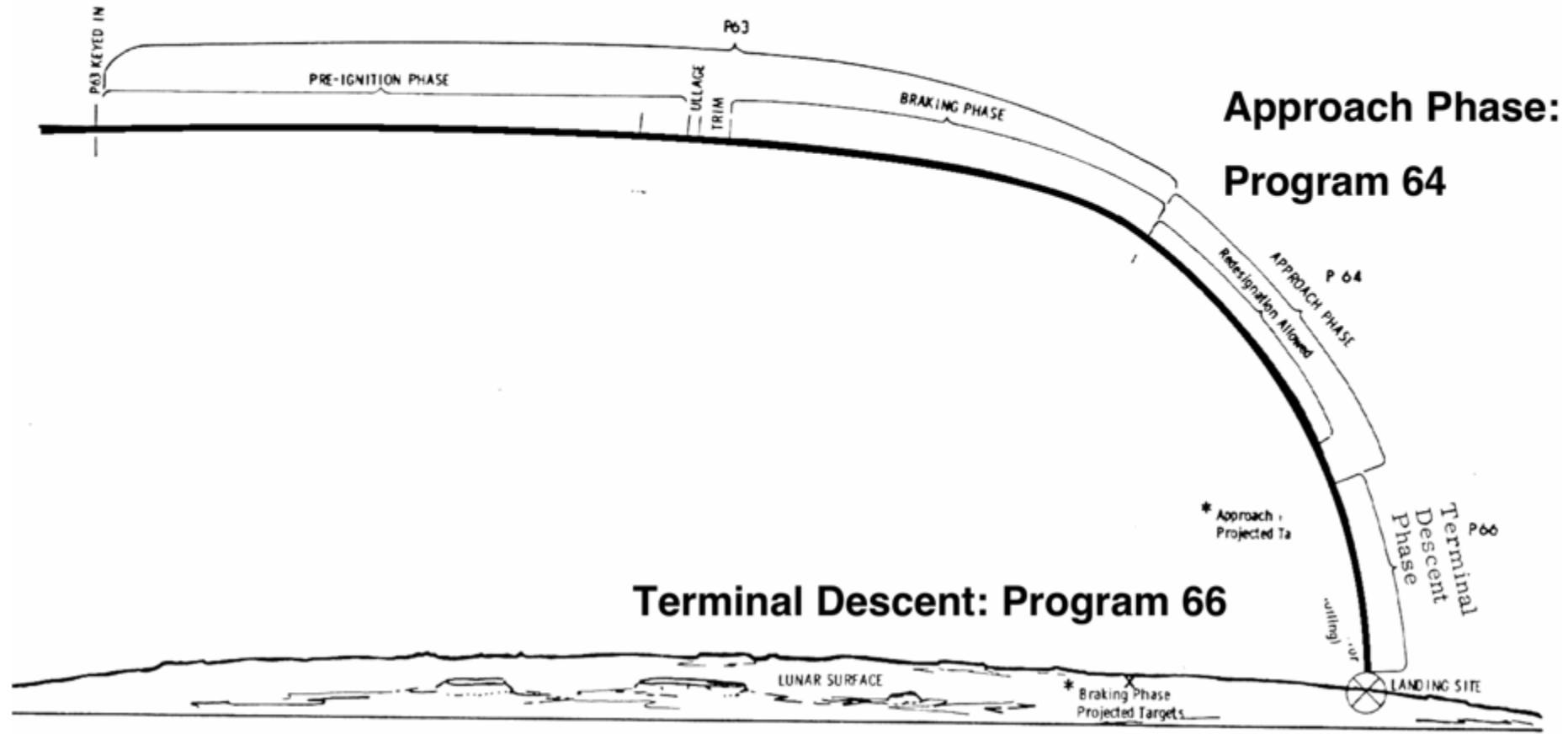
THE AGC HAD AN "APP" FOR THAT



- Mission divided into phases by velocity changes (e.g. "burns" of the main engines)
- A lunar mission involved ~11 burns
- For each unique maneuver, there was a *major mode program* to handle it

Lunar Module Descent Profile

Braking Phase: Program 63



EXAMPLE OF GUIDANCE ROUTINE SOFTWARE DEVELOPMENT WORKFLOW – EPHEMERIS ROUTINES

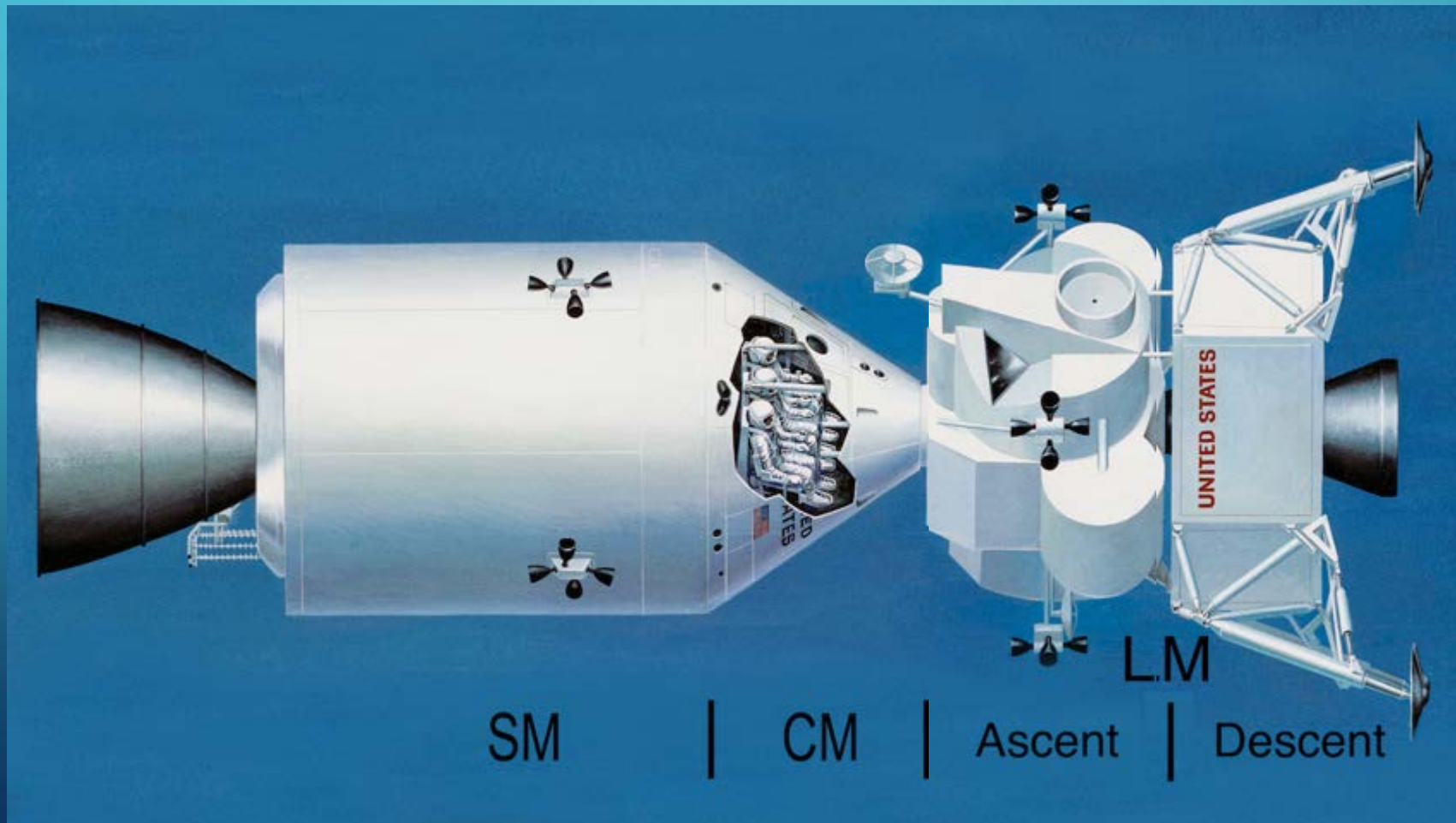
- Knowing the position of the moon (Ephemeris) at any moment
 - Accurately, over a sufficiently long time period (2 weeks), minimizing resource usage
- Where do you get the “ground truth” data to test an algorithm?
 - Classically studied problem (Newton, Euler, Lagrange, Laplace, Delaunay...)
 - Brown’s Lunar Theory (1897) + Tables (1919) + data from main-frame codes using Fourier series
- Polynomial fit to X, Y, Z positional data
 - 8 double precision coefficients for each of X, Y and Z → **48 words**
 - AGC Interpreter subroutine → **86 words**
 - Initially in MAC language Honeywell 1800 mainframe

INFRASTRUCTURE SOFTWARE

Program Name	Purpose	Size (AGC words)
Executive ²⁵	Priority-driven large/long-running process manager	~350
Waitlist ²⁶	Time-sequenced small/short-running process manager	~300
Down-Telemetry ²⁹	Transmit system data to ground	~200
Restart ^{30,31,32}	Error recovery and restart protection	~1225
Interpreter ²⁷	Space guidance domain-specific programming language interpreter	~2200
DSKY I/O ²⁸	Cockpit displays and keypad	~3500
Combined Total	22% of fixed memory	~7775

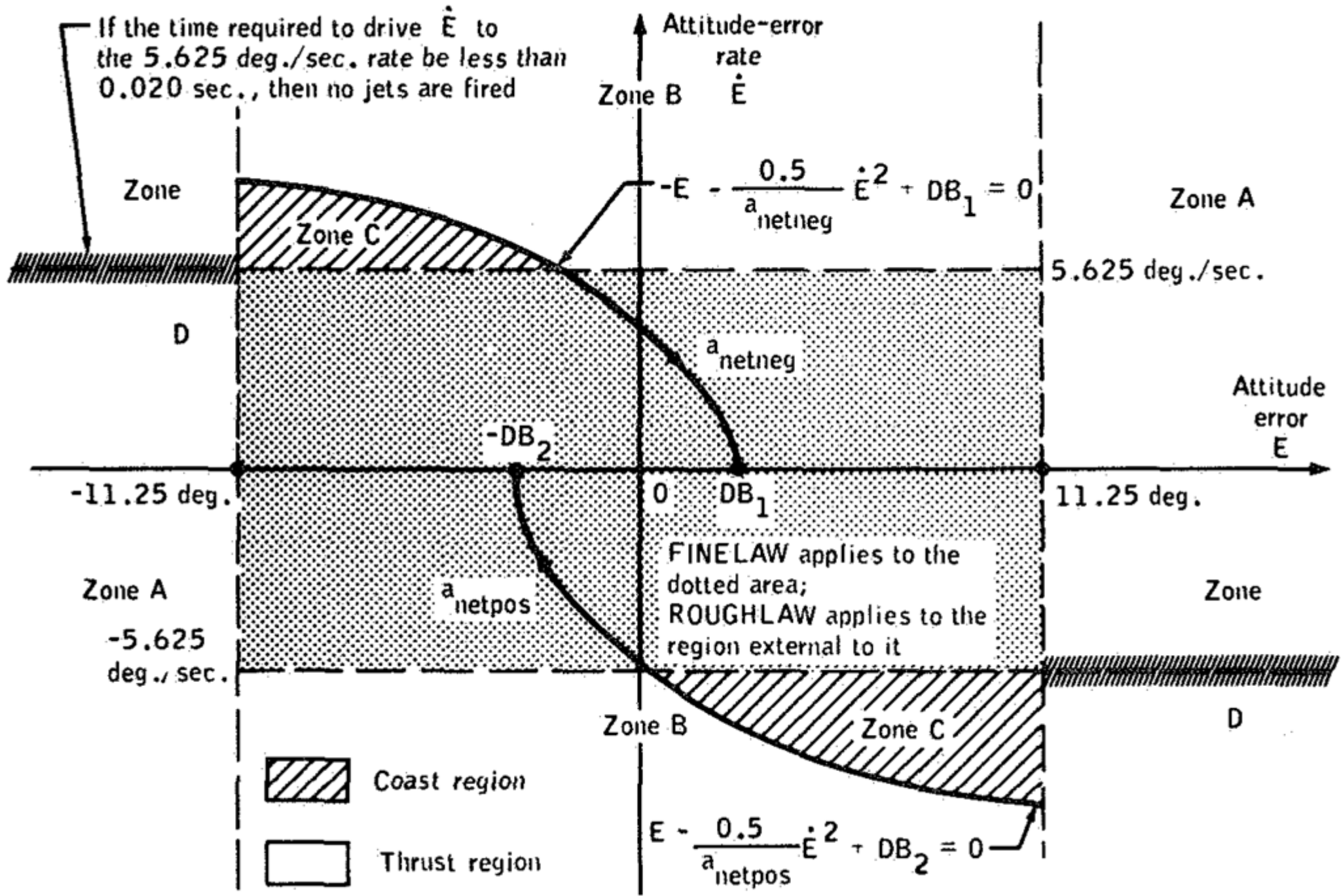
APOLLO'S DIGITAL AUTO PILOTS: A PERFORMANCE PORTABILITY CHALLENGE

- Many different hardware configurations
- One implementation



KALMAN FILTERING AND THE DIGITAL AUTO PILOTS (DAP)

- Prediction phase: Use idealized model for spacecraft motion
 - Comparison phase: Measured state from sensors compared with predicted
 - Control decisions based on the difference
-
- Performance Portability: Switch settings and pre-programmed parameters



BLOCK II – AUTOPILOT CONFIGURATION DATA (Noun 46)

DAP data loaded into components R1 and R2 upon request by flashing V06N46.

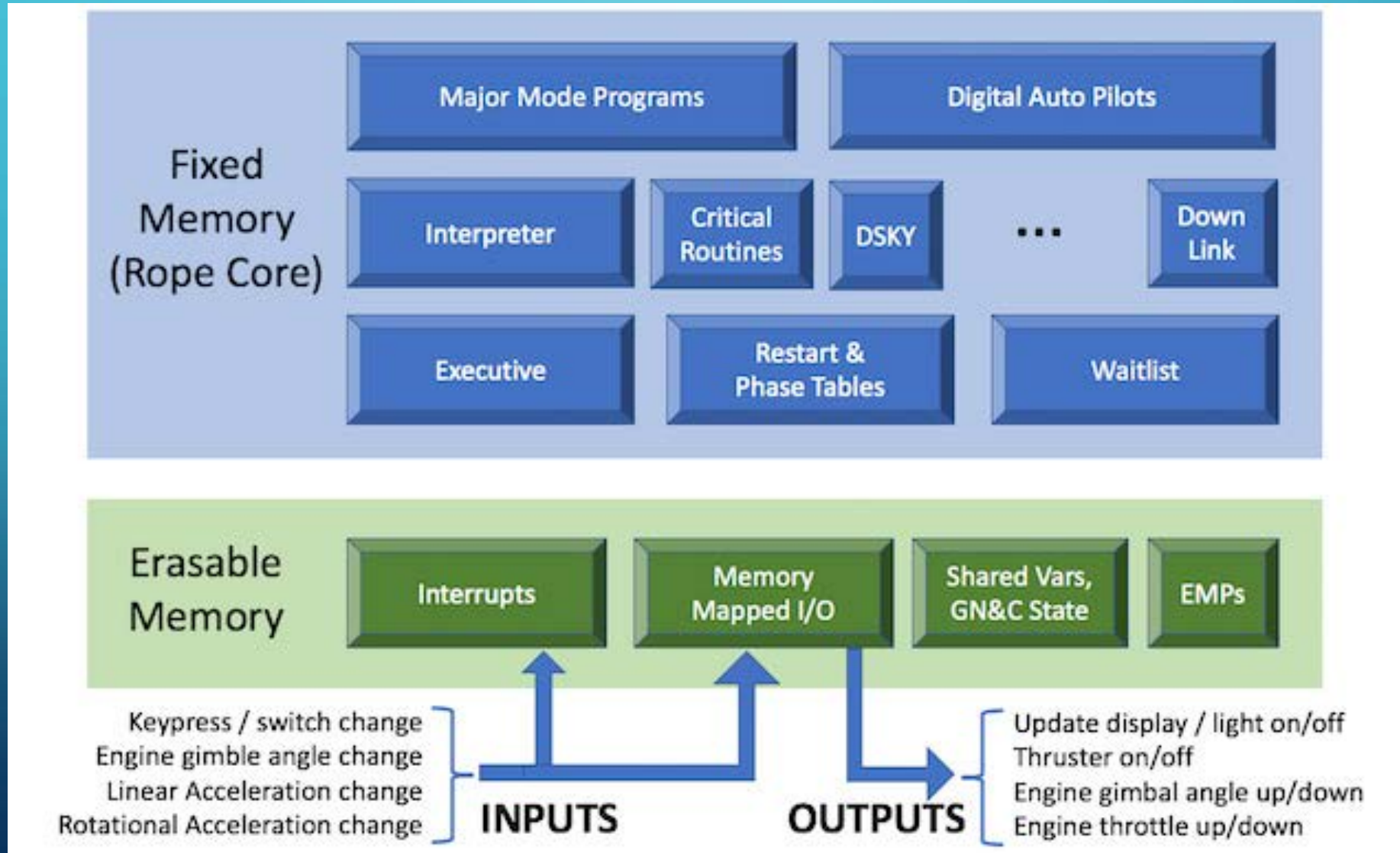
R1 = ABCDE (DAPDATR1)

A	B	C	D	E
Vehicle Config.	X-transl for Quad A/C	X-transl for Quad B/D	Attitude Deadband	Maneuver Rate
0=No DAP 1=CSM 2=CSM & LM 3=CSM & SIVB 6=CSM & LM (ascent stage only)	0=Disable A/C 1=Use A/C	0=Disable B/D 1=Use B/D	0=+/-0.5 deg 1=+/-5.0 deg	0=0.05 deg/s 2=0.5 deg/s 3=2.0 deg/s

R2 = ABCDE (DAPDATR2)

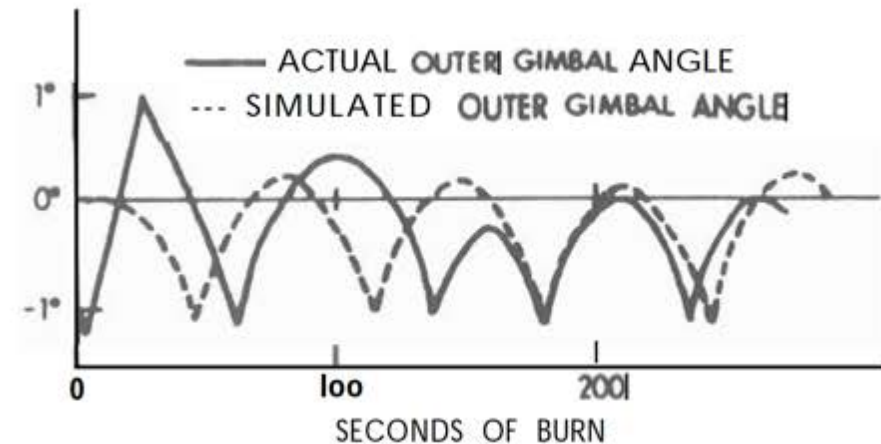
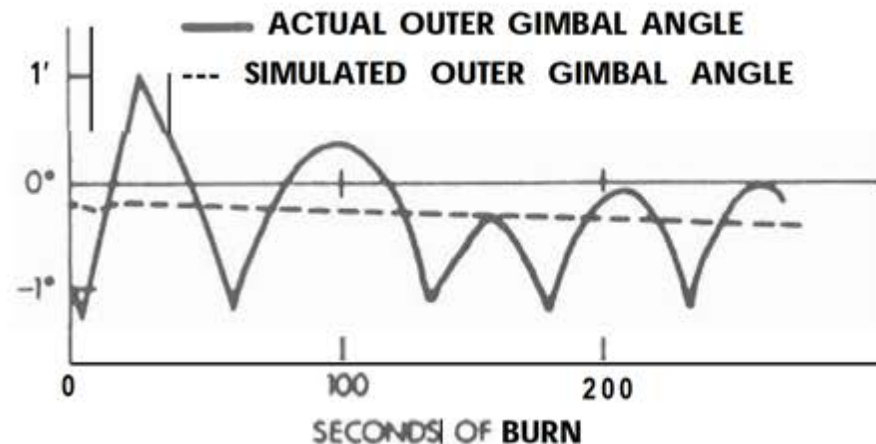
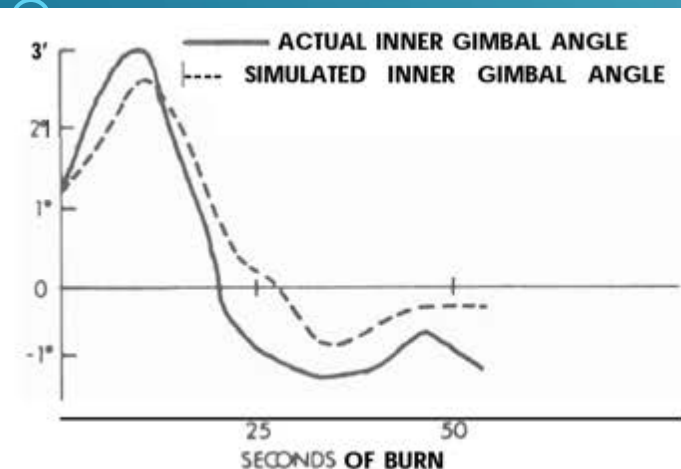
A	B	C	D	E
Roll Quad Select	Quad A Status	Quad B Status	Quad C Status	Quad D Status
0=Use B/D 1=Use A/C	0=Disable 1=Use	0=Disable 1=Use	0=Disable 1=Use	0=Disable 1=Use

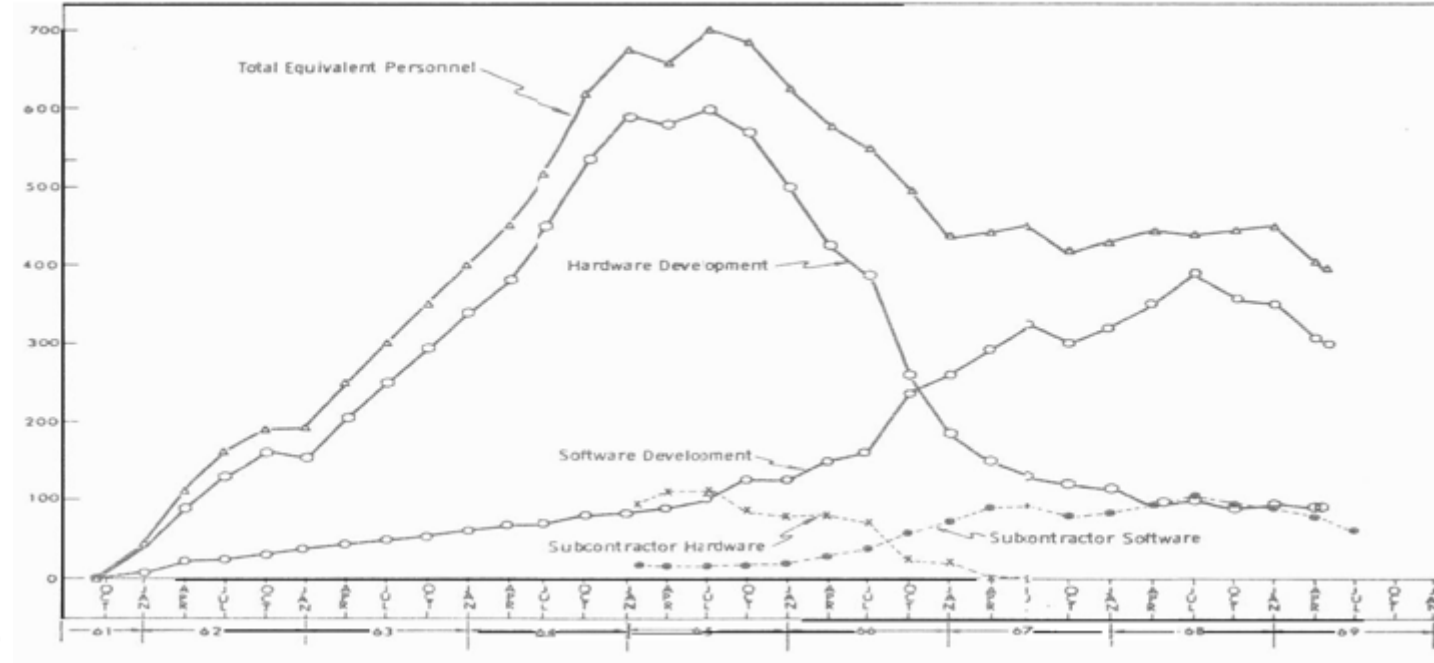
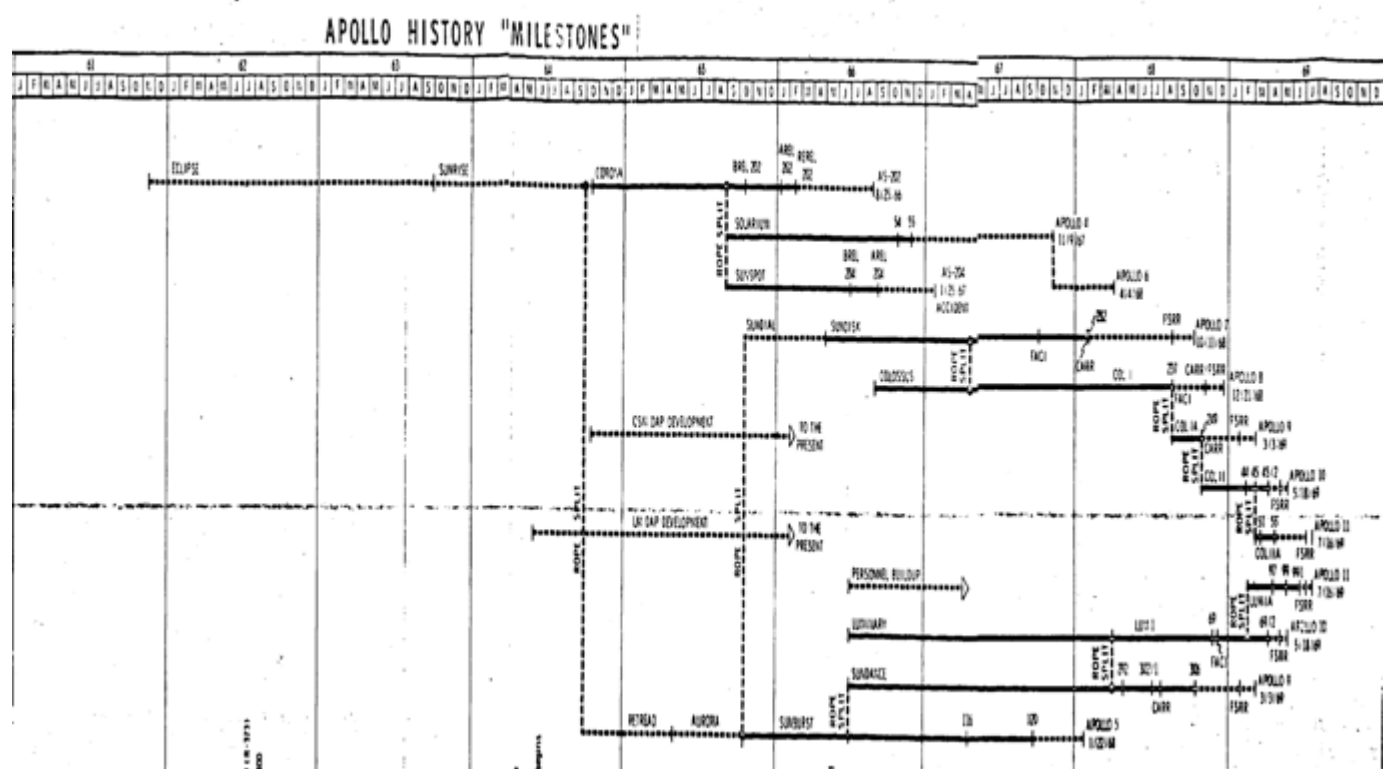
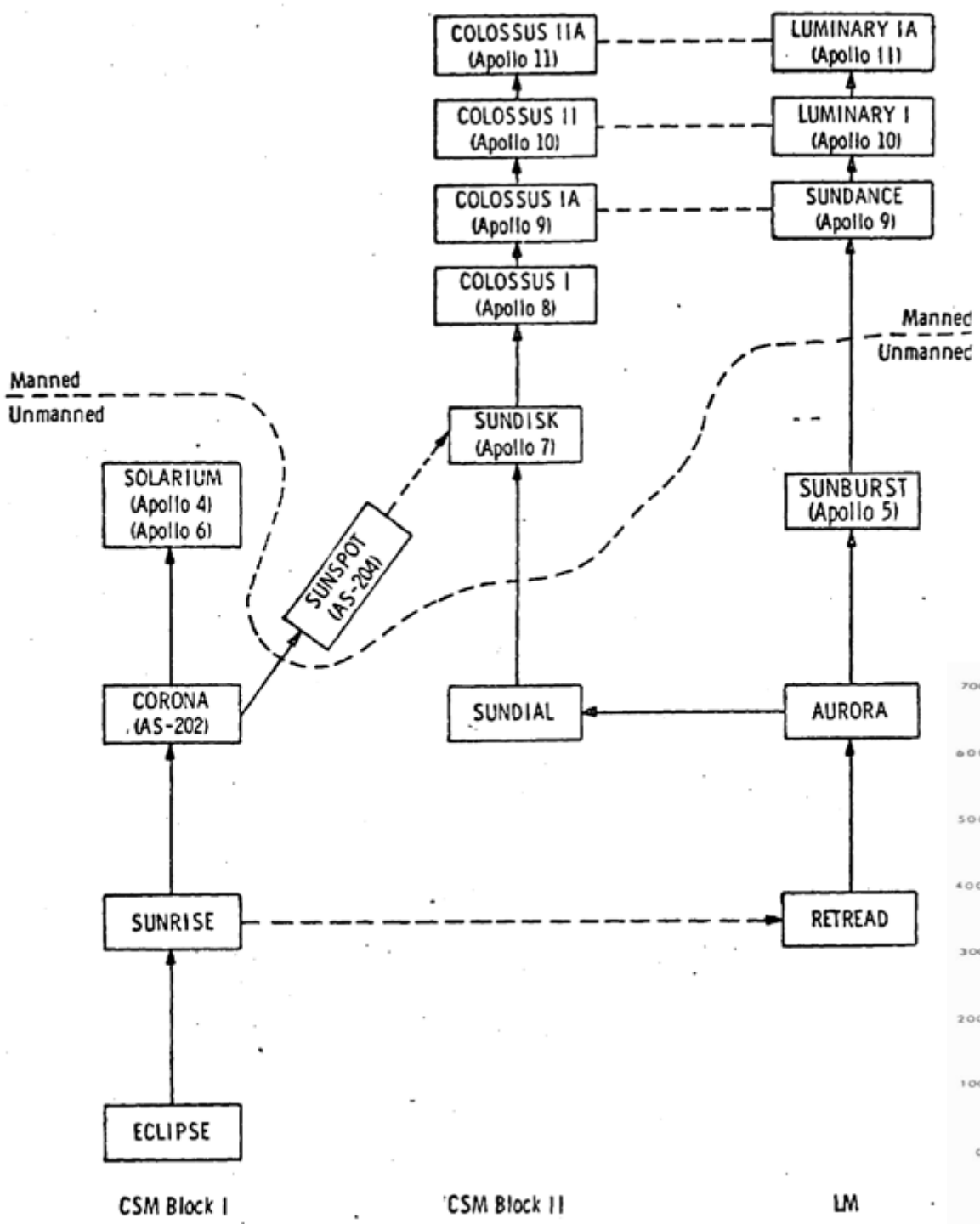
AGC SOFTWARE "STACK"

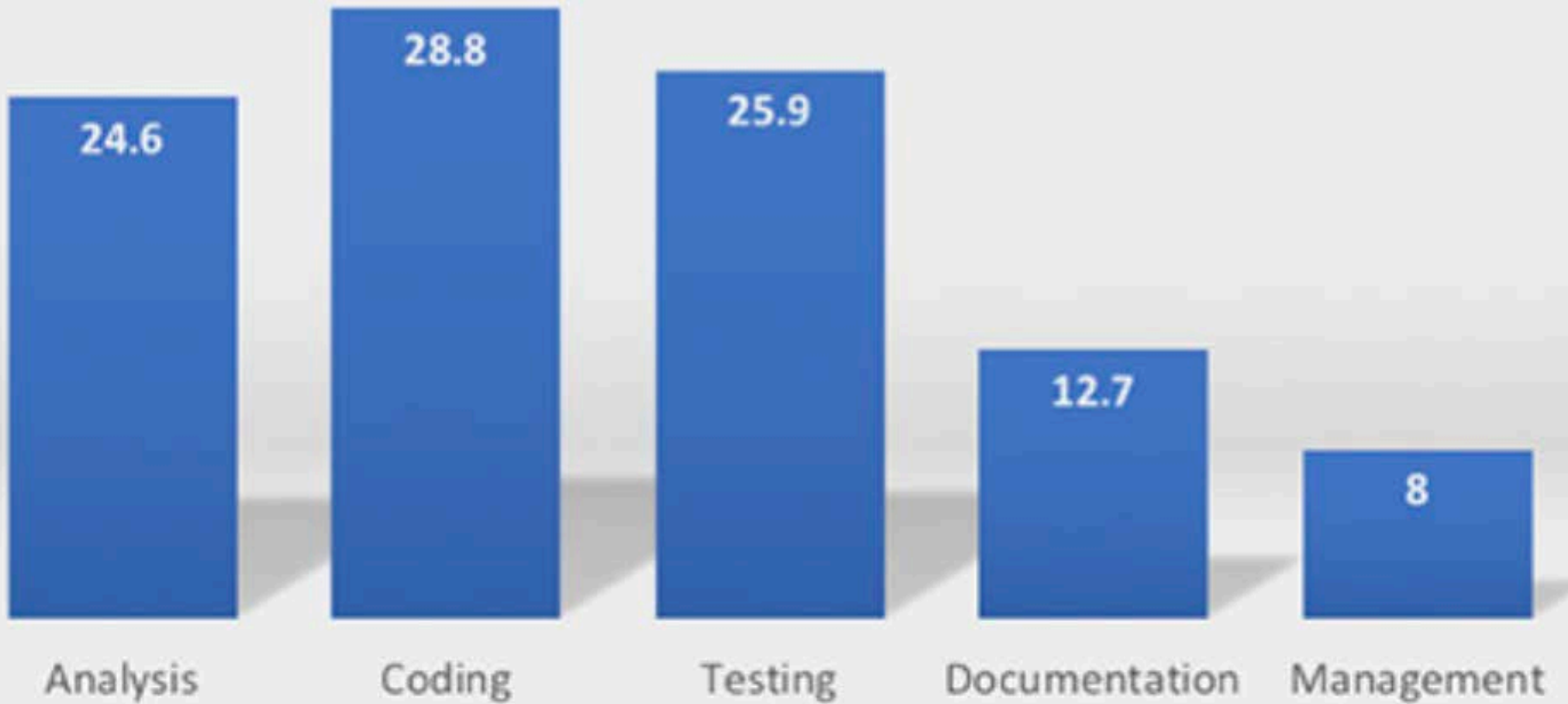


TESTING

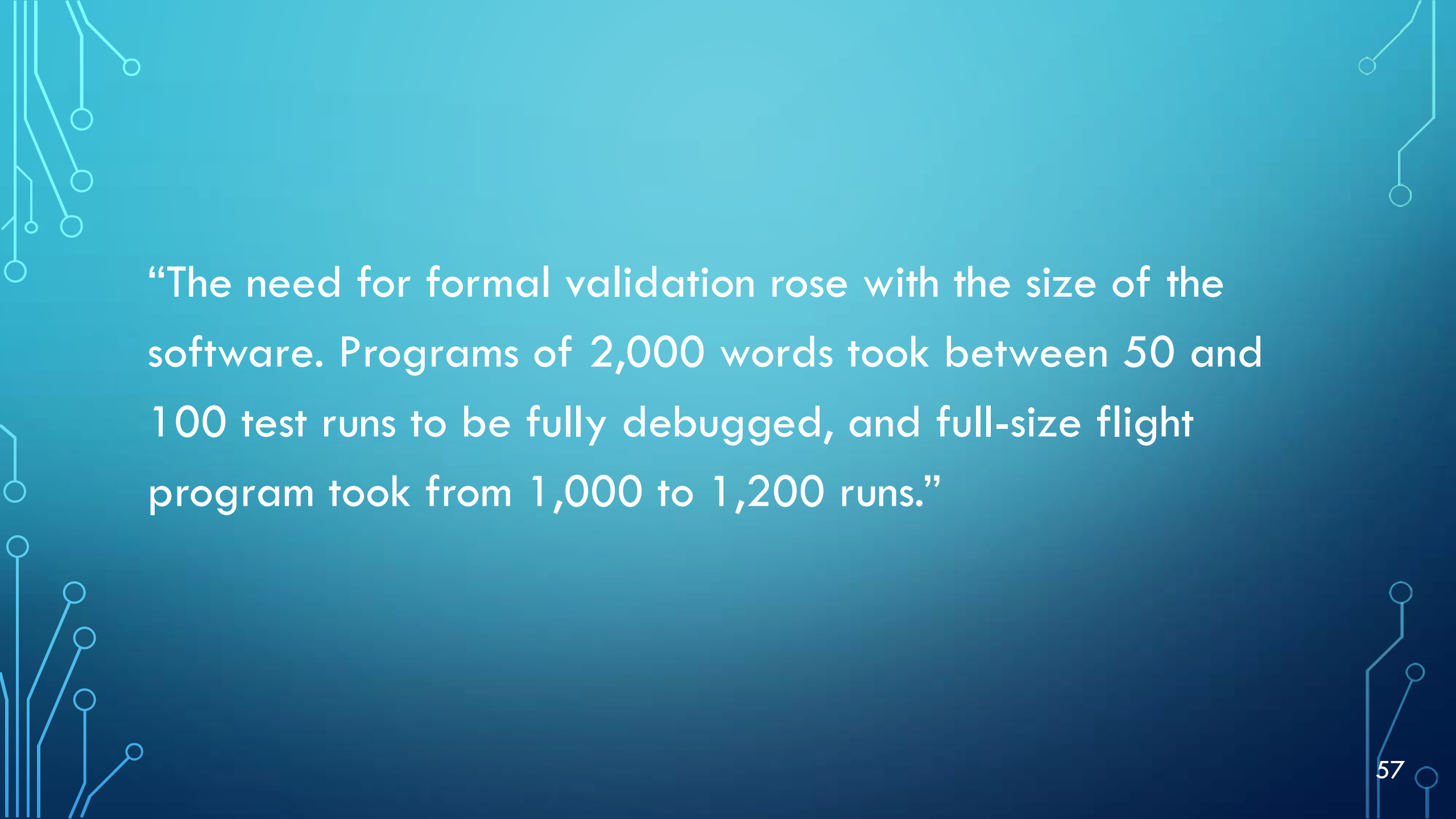
- All-digital simulator: (AGC + Devices + Spacecraft “Environment”)
 - 1 HW-800, 2 HW-1800, 2 IBM 360-75 (4,500 equiv. HW-1800 hours/month)
- Several other levels of testing
 - Hybrid Simulator: Real AGC + Analog Computer (two story building)
 - Flight simulators & Crew Rehearsals
- Actual flight testing in mission plans







Project	1965 (\$M)	2019 (\$M)	2019 \$M/yr
Apollo (10 yr)	25,000	203,000	20,300
PGNCS (10 yr)	600	~5000	500
Software (5 yr)	60	~500	100

The background is a dark teal gradient. In the corners, there are decorative white line-art patterns resembling circuit traces or neural network connections, with small circles at the end of the lines.

“The need for formal validation rose with the size of the software. Programs of 2,000 words took between 50 and 100 test runs to be fully debugged, and full-size flight program took from 1,000 to 1,200 runs.”

“SOFTWARE ENGINEERING”

- Margaret Hamilton, lead developer of Lunar Module flight program introduced this term...

“...to bring the software [effort] legitimacy so that it and those building it would be given due respect”



“No one doubted the quality of the process used in development that can

Five lessons were identified:

1. up-to-date documentation is crucial
2. verification must proceed through s
3. requirements must be clearly defin
4. good development plans should be
5. more programmers do not mean fo

APOLLO

GUIDANCE AND NAVIGATION

Approved: J. L. Nevins, Jr. Date: 5/2/66
JAMES L. NEVINS, JR., ASSISTANT DIRECTOR
INSTRUMENTATION LABORATORY

Approved: David G. Hoag Date: 2 May 66
DAVID G. HOAG, DIRECTOR
APOLLO GUIDANCE AND NAVIGATION PROGRAM

Approved: Ralph R. Ragan Date: 2 May 66
RALPH R. RAGAN, DEPUTY DIRECTOR
INSTRUMENTATION LABORATORY

E-1956
AN AUTOMATED DOCUMENTATION
TECHNIQUE FOR INTEGRATING
APOLLO CREW PROCEDURES
AND COMPUTER LOGIC

by
J. C. Dunbar, R. A. Larson,*
P. T. Augart

Presented at the 7th IEEE
Symposium on Human Factors
in Electronics, Minneapolis,
Minnesota, May 5, 6, 1966

*AC Electronics Resident Engineer

MIT INSTRUMENTATION
LABORATORY
CAMBRIDGE 39, MASSACHUSETTS

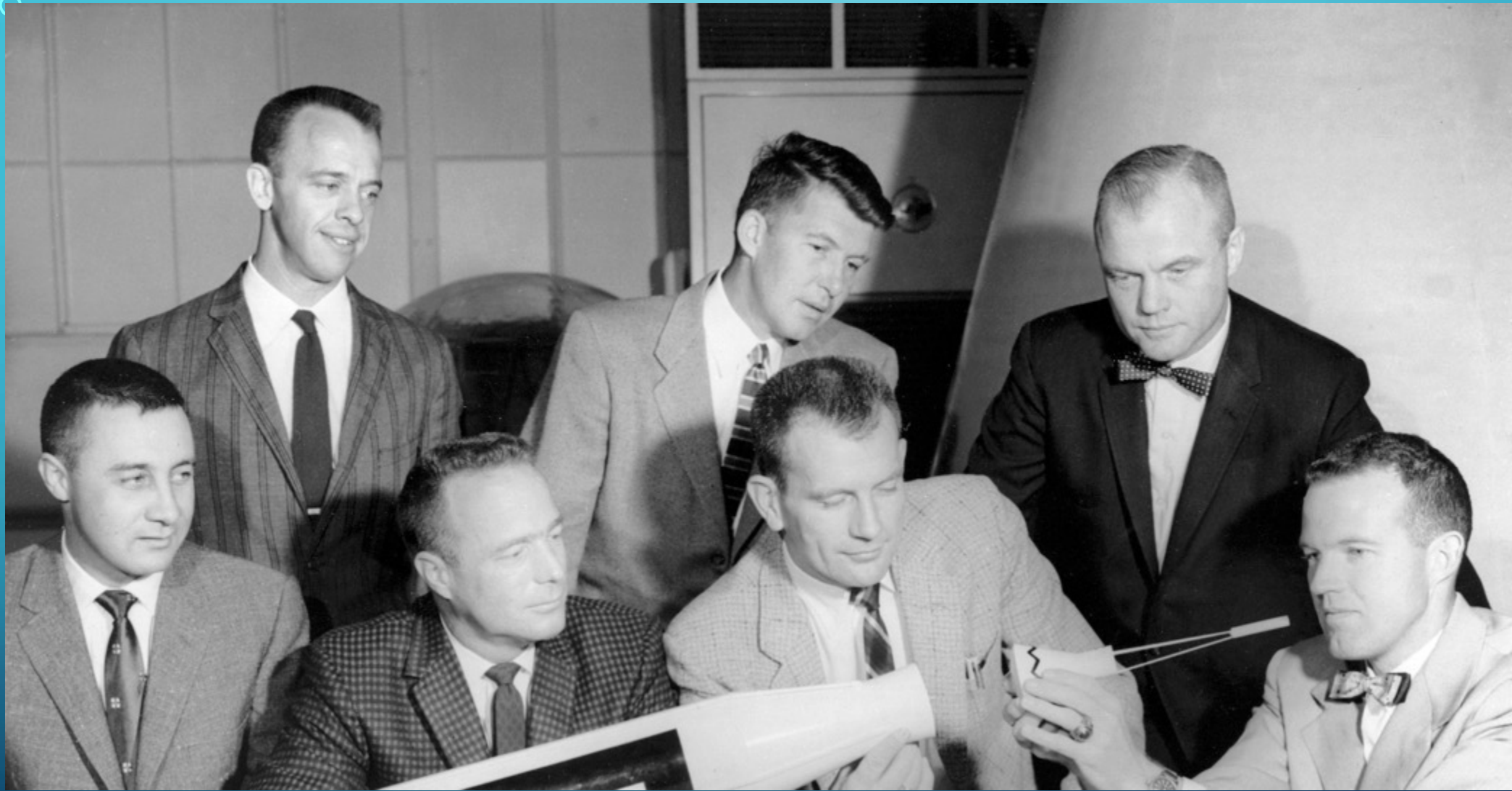
© Copyright by the Massachusetts Institute of Technology.
Published by the Instrumentation Laboratory, Massachusetts
Institute of Technology, Cambridge Massachusetts 02139.
Printed in the United States of America.

COPY # 166

OUTLINE

- Background
- Hardware Architecture
- Guidance Software
- Brief Detour
- Mission Applications





Mercury 7 Astronauts



Gemini Astronauts



Apollo GN&C System (MIT-IL)



Apollo Astronauts



Apollo Mission Control



Apollo 11

Apollo 12

Apollo 14

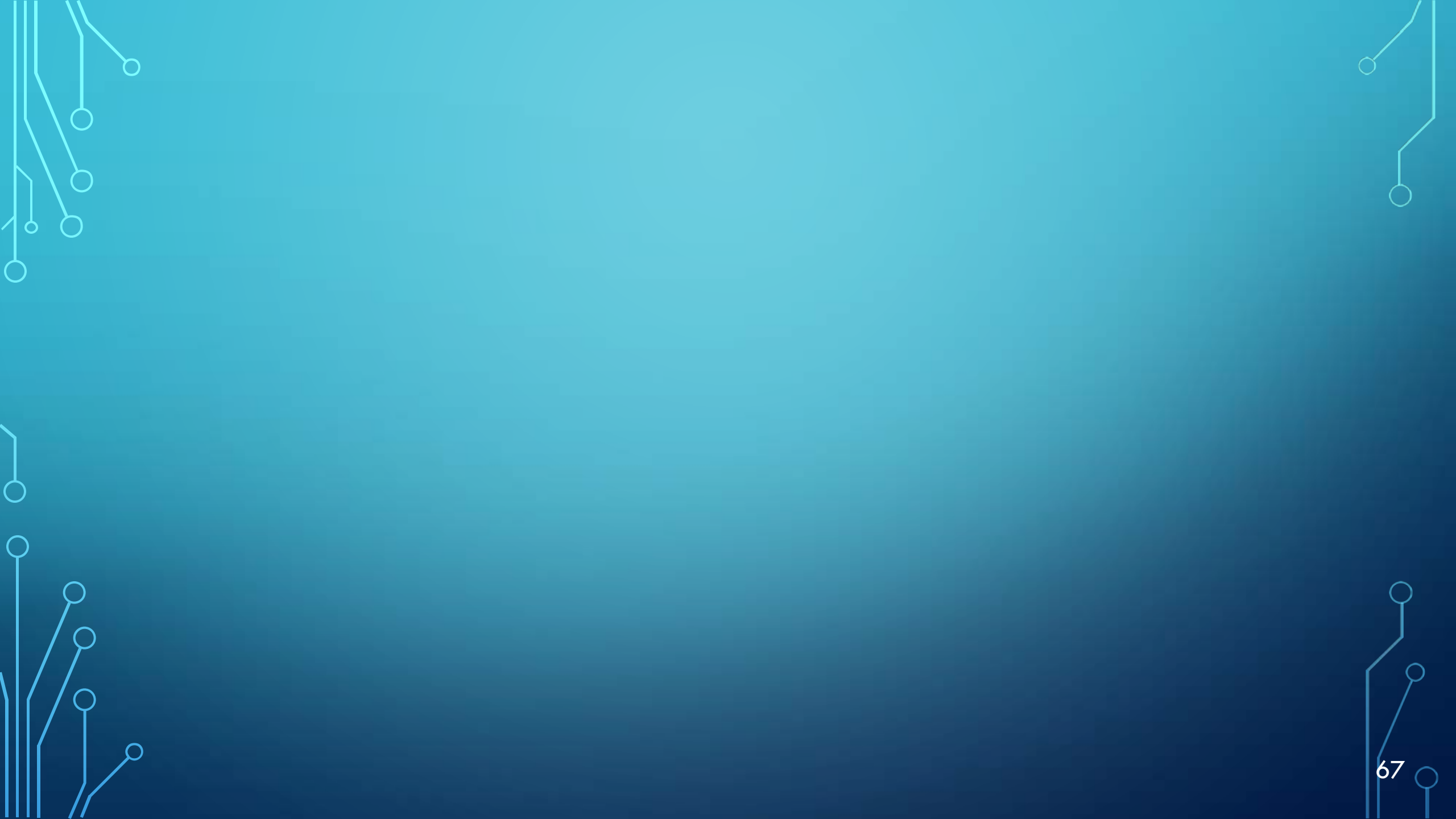


Apollo 15

Apollo 16

Apollo 17

Apollo Moonwalkers





RHEA HURRELL ALLISON



JANE HART



MARY WALLACE FUNK II



JEAN HIXSON



MYRTLE CAGLE



IRENE LEVERTON



SARAH LEE GORELICK



JAN DIETRICH



MARION DIETRICH



GENE NORA STUMBOUGH



BERNICE TRIMBLE STEADMAN



GERALDINE SLOAN

Mercury 13



Valentina Tereshkova – 3 days in orbit, 1963

A BRIEF DETOUR: WOMEN AND COMPUTERS

- 1640-1960: “Computer” → “one who calculates”
- Tedious calculation was “women’s” work (“kilogirl”)
- Before 1960: Computers were almost exclusively women



Harvard Observatory - 1890



Oak Ridge - 1942



Los Alamos - 1943



Bletchly Park - 1944

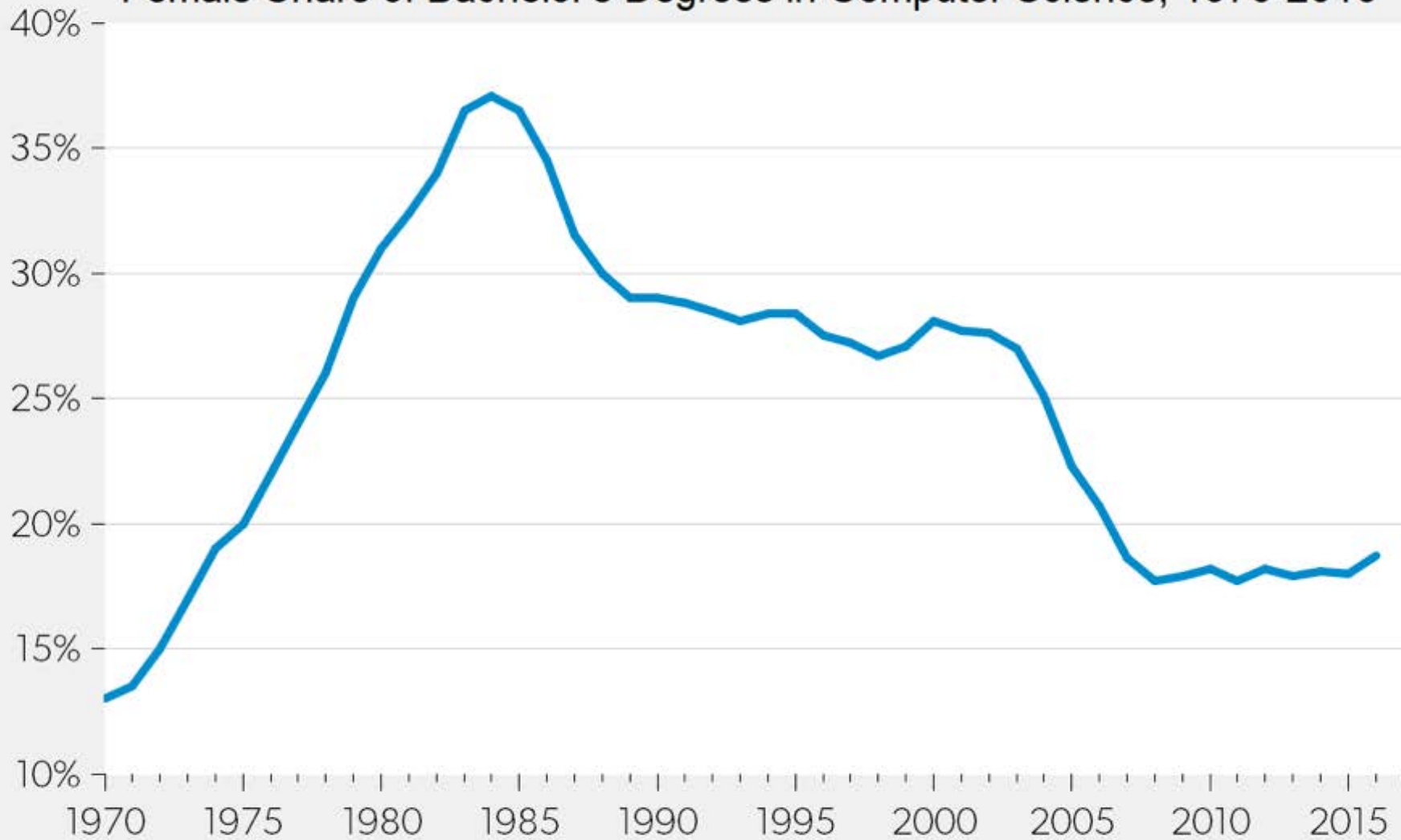


Jet Propulsion Lab - 1953



Langley, West Computing Group (1958)

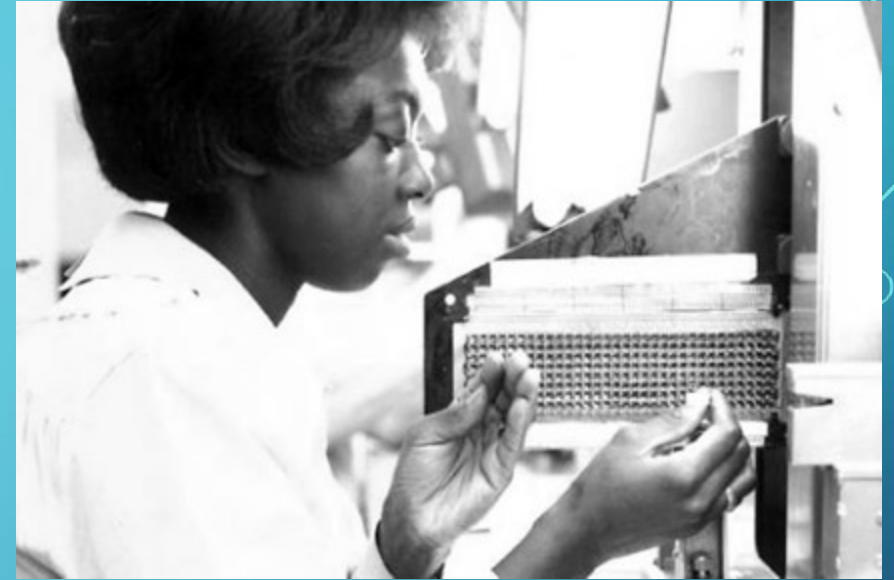
Female Share of Bachelor's Degrees in Computer Science, 1970-2016



Source: US Department of Education

A BRIEF DETOUR WOMEN IN THE AGC PROJECT

Margaret Hamilton, Phyllis Rye,
Saydean Zeldin, Elain Denniston



A BRIEF DETOUR PEOPLE OF COLOR ASTRONAUTS & IN THE AGC PROJECT



William
Mallory



Ramon
Alonso



Robert
Pinckney



Capt.
Ed Dwight



Maj.
Robert Lawrence

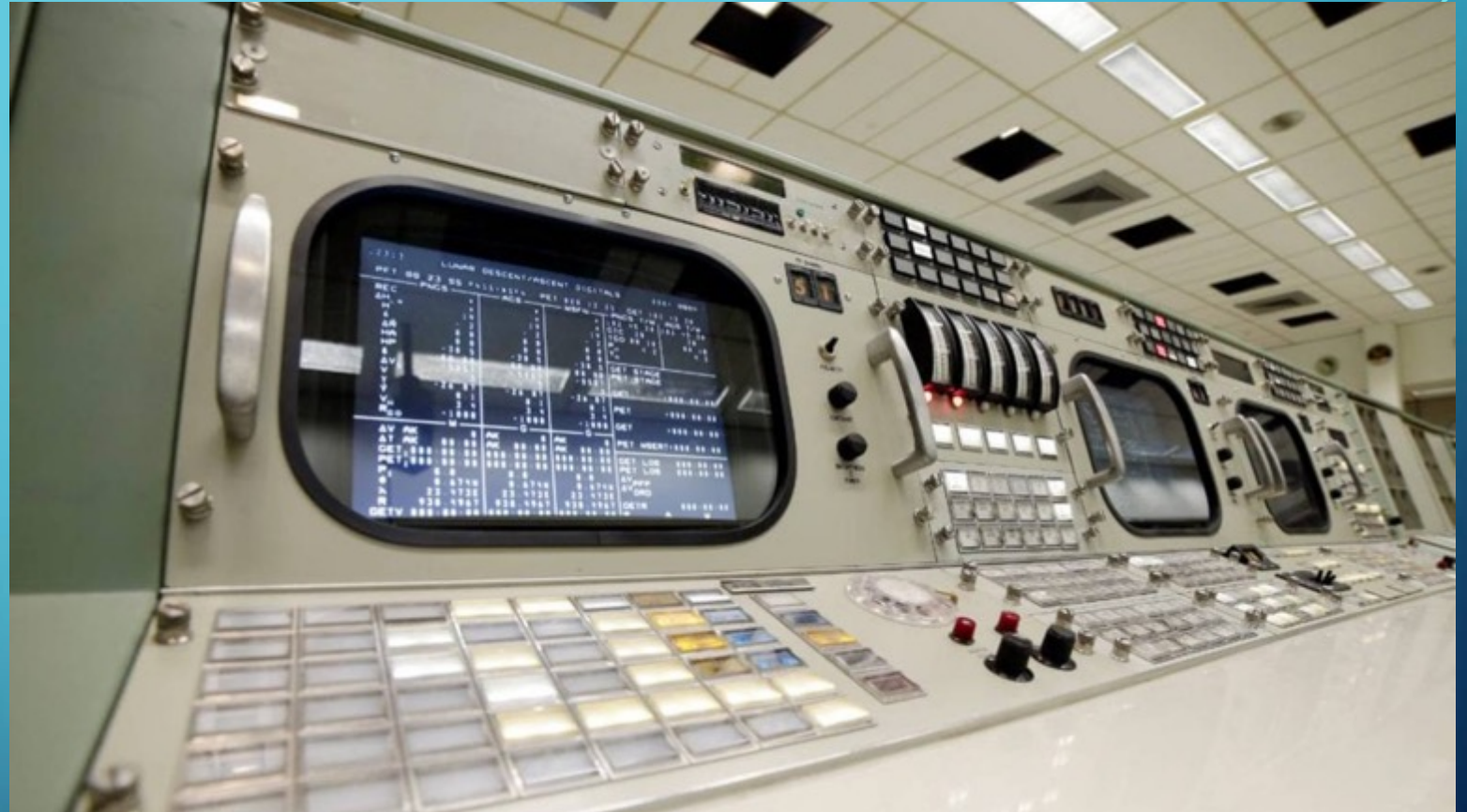
A BRIEF DETOUR: WERNHER VON BRAUN

- Creator of V2 Rocket
- Member of NAZI Party; arrested by SS for
- Captured and brought to US with ~1,600 others in 1945
- Led development of F1 engine and Saturn booster
- Championed racial integration in Wallace's Alabama



OUTLINE

- Background
- Hardware Architecture
- Guidance Software
- Brief Detour
- Mission Applications



USER INTERFACE

- VERB – NOUN
- 3, 5 char line display
- Indicator Lights
- Two in CM, one in LM one in Huston

VERB LIST		NOUN LIST		
01-05	DISPLAY OCTAL	* - LEGIT LOADABLE NOUN & DATA		58
06	DISPLAY DECIMAL	VALID ANYTIME NOUN CALLED		59
07	DP DEC DSPLY (≤N38)	V - DATA VALID ANYTIME NOUN CALLED		60
11-15	MONITOR OCTAL	L - LEGIT LOADABLE NOUN		61
16	MONITOR DECIMAL	X - LEGIT LOADABLE NOUN (HR, MIN, .01S)		62
17	DP DEC MON (≤N38)	(IF LOAD, ENTR R1, R2, R3)		63
21-25	LOAD DATA			64
27 01	DSPLY FIXED MEMORY	01, 02, 03 * SPECIFIED OCT ADRS		65 V
30	EXECUTIVE (PRE/L N26)	(DSPY OCT DEC)		
31	WAITLIST (PRE/L N26)	(N01 [OCT] [.XXXXX])		
32	RECYCLE	(N02 [OCT] [XXXXX.])		
33	PROCEED	(N03 [OCT] [.01°])		
	(REQ W/ V 21-V23)			
34	TERMINATE	04 GRAVITY ERR ✕ [.01°(R1)]		
	(EXCEPT N49, 60, 63, 88)	05 SIGHT ✕ DIFF/SV-RR LOS ✕ [.01°(R1)]		66 V/R
35	TEST LITES (P00)	06 L OPTION CODE [OCT]		67
36	FRESH START	(SEE P21, P22, P52, P57)		68
37	CHANGE PROGRAM	07 L ADRS/CHNL, BIT ID, ACTION [OCT]		69 L
40 20	ZERO ICDU'S	(SEE "FLAGWRD/CHNL SET/RESET")		70 L
40 72	ZERO RR CDU'S	08 V ALARM DATA [OCT]		
41 20	IMU CRS ALN	(ALMCADR, "BBCON", ERCOUNT)		
41 72	RR CRS ALN	09 V ALARM CODES [OCT]		
42	GYRO TORQ	(1ST, 2ND, MOST RECENT ALM)		
43	LOAD FDAI ERROR	10 * SPECIFIED CHNL [OCT(R1)]		71 L
	NEEDLES (P00)	(CAN'T 34, CAN'T 3, 4, 7, 15)		
44	TERM RR DESIGNATE	READ 35 LOAD 16, 30, 31, 32		72
47	INITIALIZE AGS	(IF LOAD CH 33, RESETS BITS 15-11)		73 L
48	DAP DATA LOAD	11 X T CSI OR T APOAPSIS [H, M, .01S]		74
49	CREW ATT MNVR (P00)	(0, 0, 0 = COMPUTE T APOAPSIS)		75
50	PLEASE PERFORM	12 L OPTN CODE [OCT (0000X, 0000Y)]		
52	REQST CURSOR MK	(X (SPFY) Y=1 Y=2)		
53	REQST SPIRAL MK	(V82 2 (VEH) LM CSM)		76 L
54	REQST X OR Y MK	(V89 3 (TK ATT) +Z +X)		
55	INCRMT CLK (H, M, S)	(V63 4 (RADAR) RR LR)		77
56	TERM TRACKING	(41 72 6 (RR FN) LOCK DESIG)		78
		13 X T CDH [H, M, .01S]		

APOLLO 11

- Russian Luna 15
- Program alarms & restarts
- Hot I/O Device
- Boulder Field & Manual Inputs
- Ascent engine arm CB

APPLICABLE TO: IN DESCENT, AVERAGE-6 ON

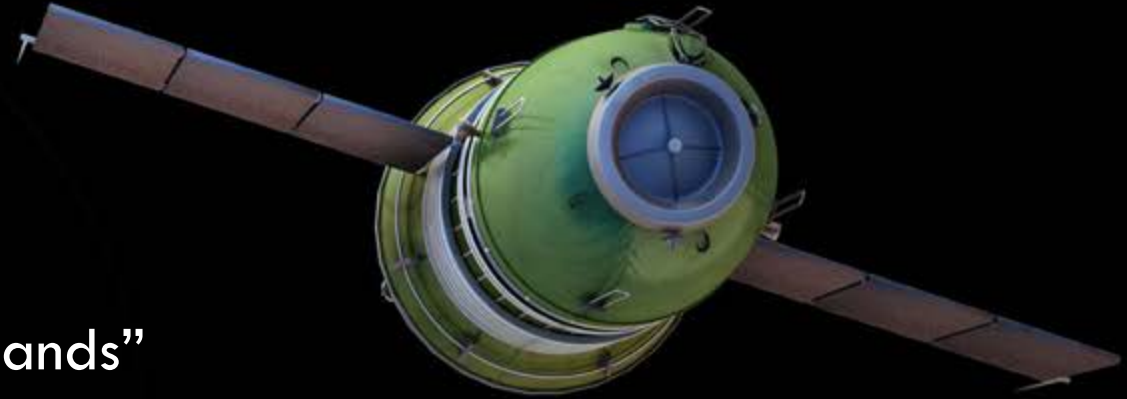
ALARM CODE	TYPE	PRE-MANUAL CAPABILITY	MANUAL CAPABILITY
0105 MK ROUT. BUSY	POODOO	*	
00430 CANT INTG. SV.	"	* PGNC'S GUID. LOST,	PGNC'S GUIDANCE NO/GO
01103 CCHOLE-PROG. BUG	"	*	
01204 NEG. WAITLIST	"	* PGNC'S/AGS ABRT/ABRT STG	(PGNC'S GO FOR
01206 DSKY, TWO USERS	"	*	TAPE METERS, CROSS-POINTERS,
01302 NEG. SQ. ROOT	"	* (decision how on	CONTROL,
01501 DSKY, PROG. BAD	"	* current rules)	ABORTING)
01502 DSKY, PROG. BUG	"	* (NO LR DATA)	(NO LR DATA)
00607 LAMB. NO SOLN	"	*	
"O.F." = Overflow, too many - CONTINUING ←			
OCCURRENCE OF:			
01104 DELAY ROUT. O.V.	BAILOUT	DUTY CYCLE MAY DEGRADE	
01201 EXECT. O.F. (VAC)	"	PGNC'S (AGS CONTROL MAY HELP - SEE BELOW)	SAME AS LEFT
01202 EXECT. O.F. (JOBS)	"	(WATCH FOR OTHER CUES)	(except "other cues"
01203 EXECT. O.F. (TASKS)	"	PGNC'S CONDITION UNKNOWN,	which would otherwise
01207 EXECT. O.F. (HRXS)	"	DSKY MAY BE LOCKED UP,	be cause for ABORT
01210 TWO USERS	"	DUTY CYCLE MAY BE UP	PROBABLY ABENT,
01211 HRK ROUT. INTRPT	"	TO POINT OF MISSING SOME	INSTEAD IT WOULD
02000 DAP O.F.	"	FUNCTIONS (NAV. LAST TO DIE)	BE PGNC'S GUIDANCE
		SWITCH TO AGS (FOLLOW ERR	NO/GO - COMPLETE MANUAL
		NEEDLES) MAY HELP (REDUCES	LANDING IN AGS.)
		PGNC'S DUTY CYCLE SIGNIF.)	
ISS WARNINGS WITH:			
00777 PIPA FAIL	LIGHT ONLY		
03777 CDU FAIL	"	PIPA/CDU/IMU FAIL	same as left
09777 PIPA, CDU FAIL	"	DISCRETES PRESENT	
07777 IMU FAIL	"		
10777 PIPA, IMU FAIL	"	(Other mission rules	

OTHER MISSION EXPERIENCES

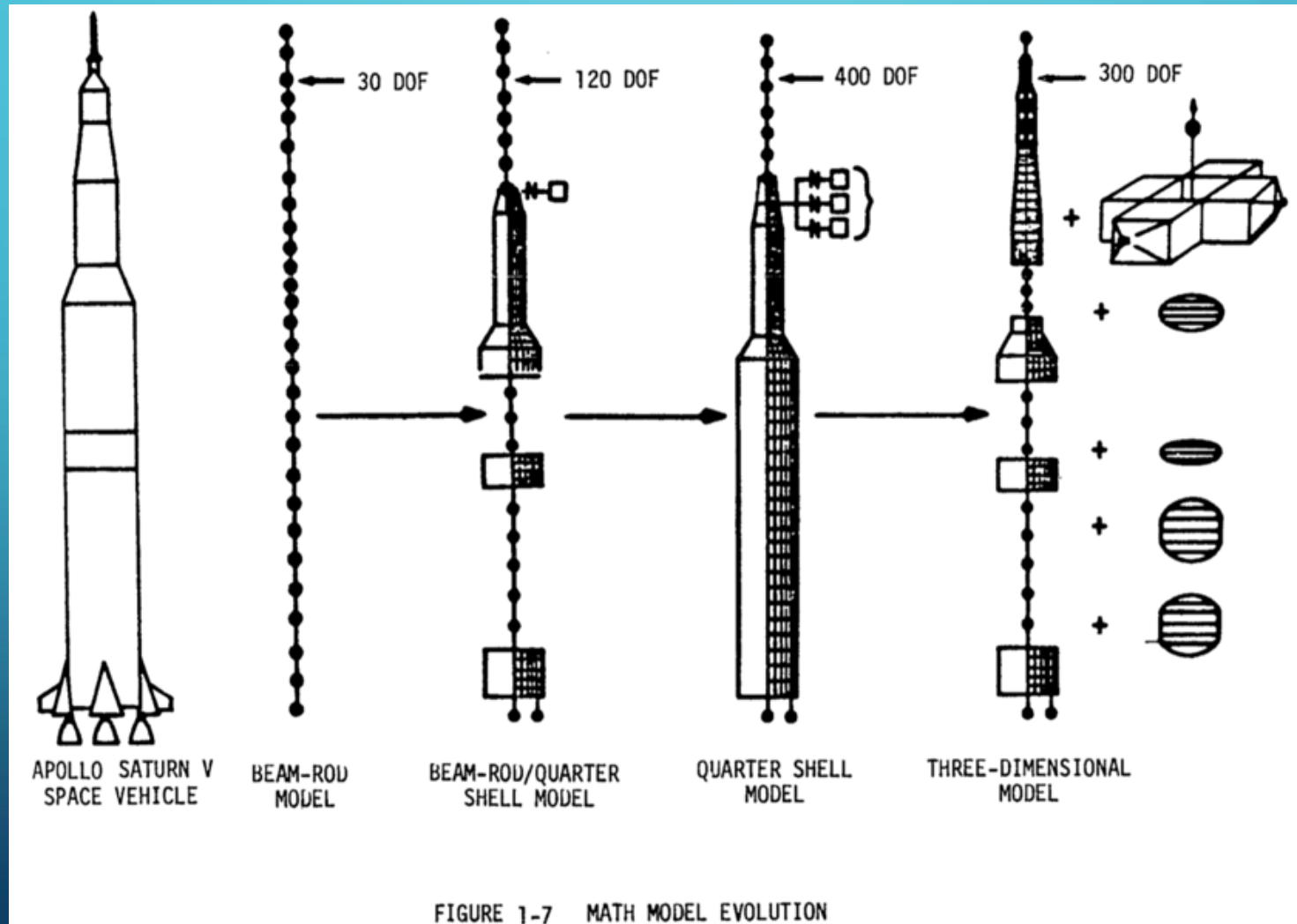
- Apollo 7
 - Schirra wants to fly re-entry manually
- Apollo 8
 - Lovell accidentally corrupts memory
- Apollo 9
 - First use of an EMP
 - Odd-ball configuration for LM test
- Apollo 10
 - Problems with barbecue roll
 - Incorrect AGS setting at staging
- Apollo 12
 - Lightning strike at launch
 - Pin-point landing near Surveyor III
- Apollo 13
 - Beneficiaries of what-if thinking
- Apollo 14
 - Abort switch fix
- Apollo 15
 - Terrain model for landing radar

ARGON-11C: RUSSIAN GUIDANCE COMPUTER

- Hybrid Integrated Circuits
- 14 bit data words, 17 bit “commands”
- 128 words RAM / 4 k-words ROM (9 kb)
- 5.2 K-Flops
- Triple redundant logic w/voting
- 34 kg / 75 watts
- 1968: Zond 7, First Russian circumlunar flight



COMPUTING WAS AN ESSENTIAL TOOL USED IN MANY ASPECTS OF THE APOLLO PROJECT



COMPUTING AND SPACEFLIGHT

- Simulation and modeling used in all major vehicle components
- Digital and Analog computers for Training simulators
- Real time computing complex (RTCC) for mission planning, tracking, weather
- Apollo both drove innovations in computing and benefited from them
- Advances in computing helped the U.S. win the Space Race

RESOURCE LINKS

- [bssw.io blog post](#)
- [Mercury 13](#) (Netfilx doc)
- [AGC Restoration](#)
- [AGC Source Code on GitHub](#)
- [Virtual AGC Project](#)
- [Ultimate AGC Talk](#)
- [Spaceflight Computing History](#)
- [AGC Software Cost Model](#)
- [Hidden Figures \(the book\)](#)
- [Hack The Moon](#)

