

# **Data Parallel Deep Learning**

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www.anl.gov

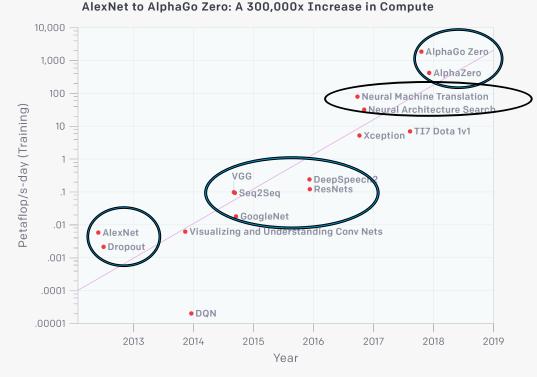
## Outline

- Why do we need for distributed / parallel deep learning on HPC
- Distribution schemes: model parallelism vs data parallelism
- Steps to change your serial code to data parallel code
- Challenges and tips on data parallel training
- I/O and data management
- Science use cases
- Hands on exercises



## Need for distributed (parallel) training on HPC

"Since 2012, the amount of compute used in the largest AI training runs has been increasing exponentially with a 3.5 month doubling time (by comparison, Moore's Law had an 18 month doubling period)." https://openai.com/blog/ai-and-compute/



Finishing a 90-epoch ImageNet-1k training with ResNet-50 on a NVIDIA M40 GPU takes 14 days. (10<sup>18</sup> SP Flops) Eras:

- Before 2012 ...
- 2012 2014: single to couple GPUs
- 2014 2016: 10 100 GPUs
- 2016 2017: large batch size training, architecture search, special hardware (etc, TPU)



#### **GPT-3**

Training time for GPT 3 = 3640 Days = 9.97 Years

It would take 355 years to train GPT-3 on a single NVIDIA Tesla V100 GPU.

OpenAl launched GPT-3 in May/2020.

Using 1,024x A100 GPUs, researchers calculated that OpenAI could have trained GPT-3 in as little as 34 days.

Estimated that it cost around \$5M in compute time to train GPT-3.

#### **D** Total Compute Used to Train Language Models

This appendix contains the calculations that were used to derive the approximate compute used to train the language models in Figure 2.2. As a simplifying assumption, we ignore the attention operation, as it typically uses less than 10% of the total compute for the models we are analyzing.

Calculations can be seen in Table D.1 and are explained within the table caption.

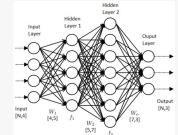
Model	Total train compute (PF-days)	Total train compute (flops)	Params (M)	Training tokens (billions)	Flops per param per token	Mult for bwd pass	Fwd-pass flops per active param per token	Frac of params active for each token
T5-Small	2.08E+00	1.80E+20	60	1,000	3	3	1	0.5
T5-Base	7.64E+00	6.60E+20	220	1,000	3	3	1	0.5
T5-Large	2.67E+01	2.31E+21	770	1,000	3	3	1	0.5
T5-3B	1.04E+02	9.00E+21	3,000	1,000	3	3	1	0.5
T5-11B	3.82E+02	3.30E+22	11,000	1,000	3	3	1	0.5
BERT-Base	1.89E+00	1.64E+20	109	250	6	3	2	1.0
BERT-Large	6.16E+00	5.33E+20	355	250	6	3	2	1.0
RoBERTa-Base	1.74E+01	1.50E+21	125	2,000	6	3	2	1.0
RoBERTa-Large	4.93E+01	4.26E+21	355	2,000	6	3	2	1.0
GPT-3 Small	2.60E+00	2.25E+20	125	300	6	3	2	1.0
GPT-3 Medium	7.42E+00	6.41E+20	356	300	6	3	2	1.0
GPT-3 Large	1.58E+01	1.37E+21	760	300	6	3	2	1.0
GPT-3 XL	2.75E+01	2.38E+21	1,320	300	6	3	2	1.0
GPT-3 2.7B	5.52E+01	4.77E+21	2,650	300	6	3	2	1.0
GPT-3 6.7B	1.39E+02	1.20E+22	6,660	300	6	3	2	1.0
GPT-3 13B	2.68E+02	2.31E+22	12,850	300	6	3	2	1.0
GPT-3 175B	3.64E+03	3.14E+23	174,600	300	6	3	2	1.0

Tom B. Brown et al, "Language Models are Few-Shot Learners", 2020.



## Need for distributed (parallel) training on HPC

- Increase of model complexity leads to dramatic increase of computation.
- Increase of the amount of dataset makes sequentially scanning the whole dataset increasingly impossible.
- The increase in computational power has been mostly coming (and will continue to come) from parallel computing.
- Coupling of deep learning to traditional HPC simulations might require distributed inference.

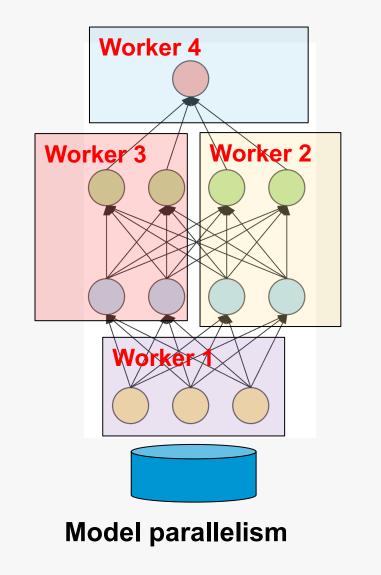


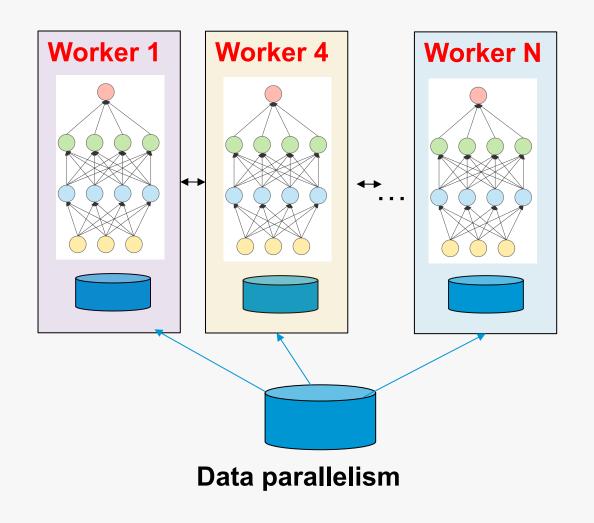






#### Parallelization schemes for distributed learning







#### Polaris

# of River Compute racks	40
# of Apollo Gen10+ Chassis	280
# of Nodes	560
# of AMD EPYC 7543P CPUs	560
# of NVIDIA A100 GPUs	2240
Total GPU HBM2 Memory	87.5TB
Total CPU DDR4 Memory	280 TB
Total NVMe SSD Capacity	1.75 PB
Interconnect	HPE Slingshot
# of Cassini NICs	1120
# of Rosetta Switches	80
Total Injection BW (w/ Cassini)	28 TB/s
Total GPU DP Tensor Core Flops	44 PF
Total Power	1.8 MW

#### T0P500 LIST - JUNE 2022

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R<sub>max</sub> and R<sub>peak</sub> values are in PFlop/s. For more details about other fields, check the TOP500 description.

**R**<sub>peak</sub> values are calculated using the advertised clock rate of the CPU. For the efficiency of the systems you should take into account the Turbo CPU clock rate where it applies.

#### $\leftarrow \quad 1\text{-}100 \quad 101\text{-}200 \quad 201\text{-}300 \quad 301\text{-}400 \quad 401\text{-}500 \quad \rightarrow$

256,592			
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#### 7 Steps to Horovod

How to change a serial code into a data parallel code:

- 1. Initialize Horovod
- 2. Pin GPU to each process
- 3. Checking pointing / printing training time on rank 0
- 4. Scale the learning rate
- 5. Set distributed optimizer / gradient tape



https://eng.uber.com/horovod/

6. Broadcast the model & optimizer parameters from rank 0 to other ranks

7. Adjusting dataset loading: number of steps (or batches) per epoch, dataset sharding, etc.



### 7 Steps to Horovod

#### Step 1. Initialize Horovod

import horovod.tensorflow.keras as hvd hvd.init()

#### **Step 2. Pin GPU to each process**

# Pin GPU to the rank - we set one GPU per process
tf.config.experimental.set\_visible\_devices(gpus[hvd.local\_rank()], 'GPU')

#### Step 3. Checkpointing on root rank

# It is important to let only one process to do the checkpointing I/O.
if hvd.rank() == 0:
 callbacks.append(tf.keras.callbacks.ModelCheckpoint('./checkpoints-km{epoch}.h5'))
if (hvd.rank()==0):
 print("Hvd Procs %d Total time: %s second" %(hvd.size(),t1-t0))



## **Gradient Descent**

Minimizing the loss:

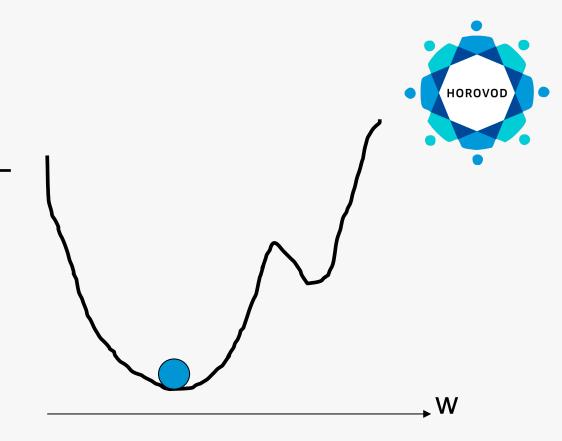
$$L(w) = \frac{1}{|X|} \sum_{x \in X} l(x, w).$$

Dataset Weight

Stochastic Gradient Descent (SGD) update

$$w_{t+1} = w_t - \eta \frac{1}{n} \sum_{x \in \mathcal{B}} \nabla l(x, w_t)$$
Minibatch

Model is updated at each step.



 One minibatch is divided into many sub minibatches and each is feed into one of the workers



#### 7 Steps to Horovod

#### Step 4: Scale the learning rate with number of workers

If we keep the local batch size on each rank the same, the global batch size increases by n times The learning rate should increase proportionally

When the minibatch size is multiplied by k, multiply the learning rate by k.

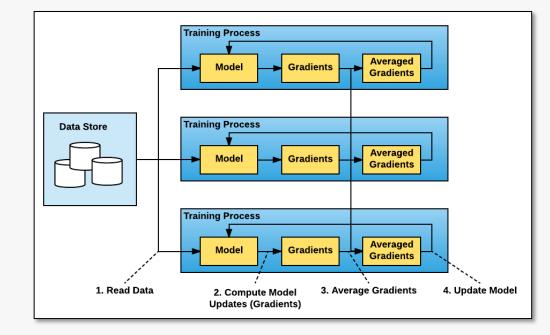
opt = tf.optimizers.Adam(lr \* hvd.size())

#### Step 5 : Wrap tf.optimizer with Horovod DistributedOptimizer

opt = hvd.DistributedOptimizer(opt)

Gradients are aggregated over all the workers through MPI\_Allreduce Gradients are averaged at each step (not each epoch)





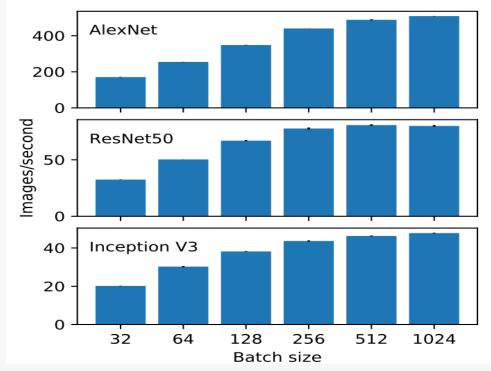


## Large minibatch training

$$w_{t+1} = w_t - \eta \frac{1}{n} \sum_{x \in \mathcal{B}} \nabla l(x, w_t)$$

$$\uparrow$$
Minibatch

- Option 1. Keeping the same global minibatch size with each worker processing B/N batch (strong scaling)
- Option 2. Increasing the global minibatch size by N times, so that each worker processes batches of size B (week scaling)



Per node throughput of different local batch size

H. Zheng, https://www.alcf.anl.gov/files/Zheng\_SDL\_ML\_Frameworks\_1.pdf

- 1. Decrease of local batch size reduces the per node throughput;
- 2. Increase of global minibatch size reduces the number of updates on each epoch (n=X/B); thus it increases the compute/communication ratio

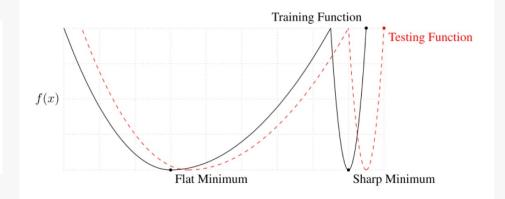


### **Challenges with large batch training**

- Convergence issue: at the initial stages of training, the model is far away from optimal solution  $\nabla l(x, \omega_{t+j}) \sim \nabla l(x, \omega_t)$  breaks down. Training is not stable with large learning rate in the beginning;
- Generalization gap: large batch size training tends to be trapped at local minimum with lower testing accuracy (generalize worse).

	Training	Accuracy	Testing A	Accuracy
Name	SB	LB	SB	LB
$F_1$	$99.66\% \pm 0.05\%$	$99.92\% \pm 0.01\%$	$98.03\% \pm 0.07\%$	$97.81\% \pm 0.07\%$
$F_2$	$99.99\% \pm 0.03\%$	$98.35\% \pm 2.08\%$	$64.02\% \pm 0.2\%$	$59.45\% \pm 1.05\%$
$C_1$	$99.89\% \pm 0.02\%$	$99.66\% \pm 0.2\%$	$80.04\%\pm 0.12\%$	$77.26\% \pm 0.42\%$
$C_2$	$99.99\% \pm 0.04\%$	$99.99\% \pm 0.01\%$	$89.24\% \pm 0.12\%$	$87.26\% \pm 0.07\%$
$C_3$	$99.56\% \pm 0.44\%$	$99.88\% \pm 0.30\%$	$49.58\% \pm 0.39\%$	$46.45\%\pm 0.43\%$
$C_4$	$99.10\% \pm 1.23\%$	$99.57\% \pm 1.84\%$	$63.08\% \pm 0.5\%$	$57.81\% \pm 0.17\%$

Performance of small-batch (SB) and large-batch (LB) variants of ADAM on the 6 networks



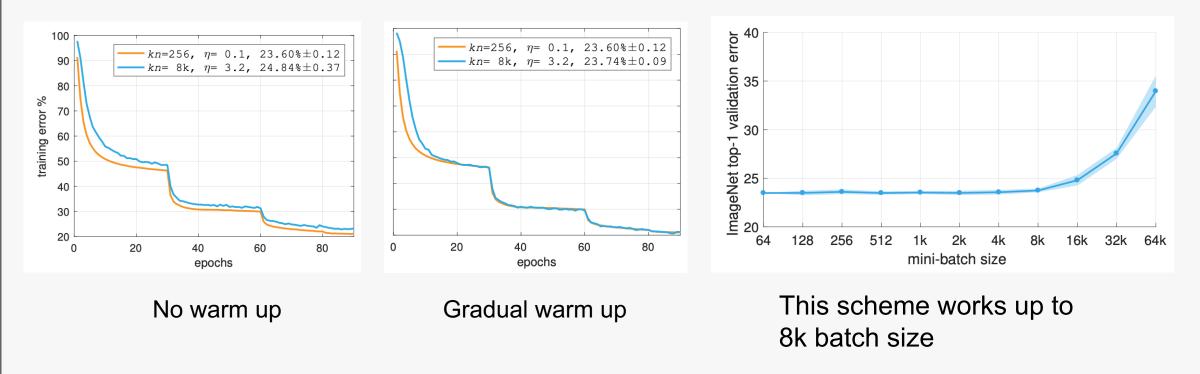
"... large-batch ... converge to sharp minimizers of the training function ... In contrast, small-batch methods converge to flat minimizers"



## **Challenges with large batch training**

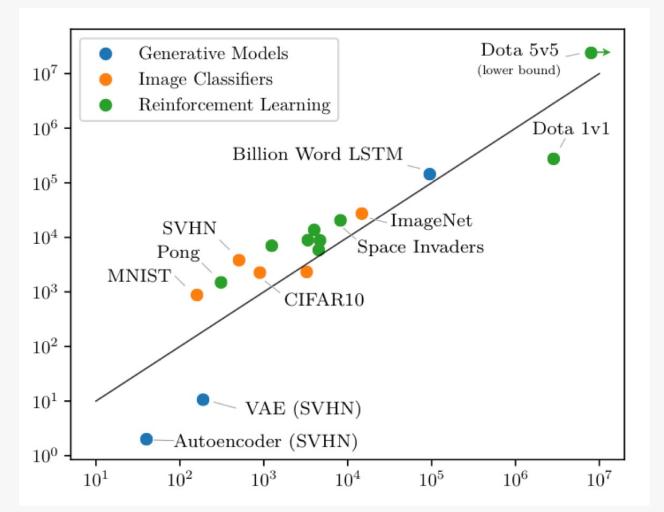
Solutions: using warm up steps

- Using a smaller learning rate at the initial stage of training (couple epochs), and gradually increase to  $\hat{\eta} = N\eta$
- Using linear scaling of learning rate ( $\hat{\eta} = N\eta$ )





## **Challenges with large batch training**



Predicted critical **maximum batch size** beyond which the model does not perform well.

S. McCandlish, J. Kaplan, D. Amodei, arXiv:1812.06162

Maximum batch size place limit to data parallel:

N\_workers < Maximum batch size



#### 7 Steps to Horovod

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6. Broadcast the model & optimizer parameters to other rank

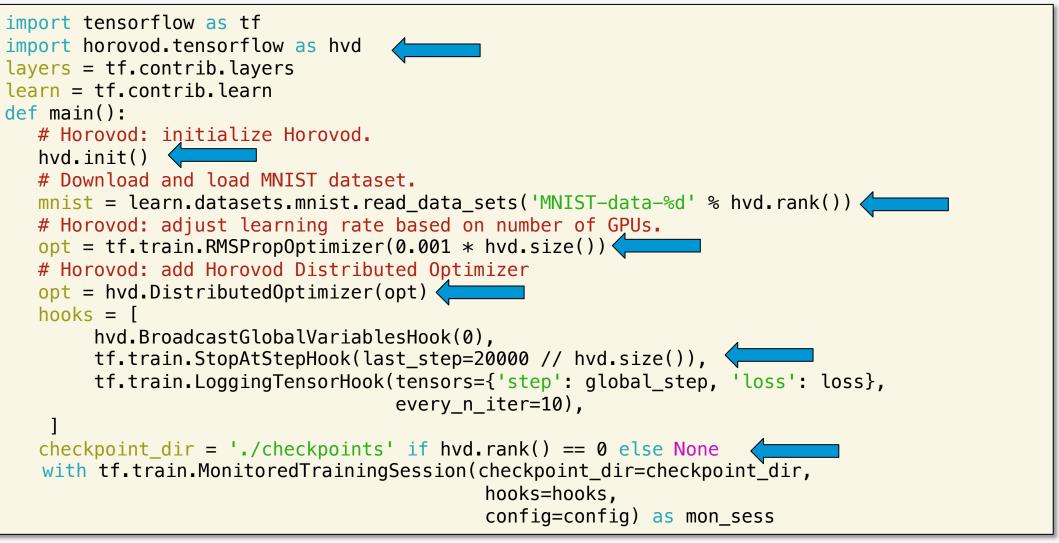
```
callbacks = [ # broad cast
    hvd.callbacks.BroadcastGlobalVariablesCallback(0),
    # Average metric at the end of every epoch
    hvd.callbacks.MetricAverageCallback(),
    # Warmup
    hvd.callbacks.LearningRateWarmupCallback(warmup_epochs=3,initial_lr=_lr),
]
```

7. Adjusting dataset loading: number of steps (or batches) per epoch, dataset sharding, etc.

```
steps_per_epoch=60000/hvd.size()/batch_size
```



### **Tensorflow with Horovod**



More examples can be found in https://github.com/uber/horovod/blob/master/examples/



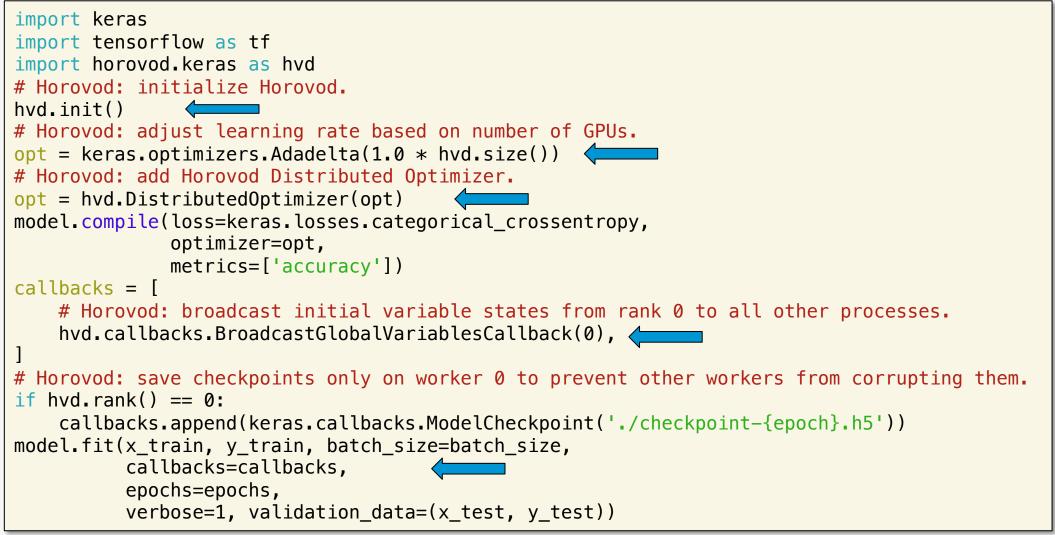
## **PyTorch with Horovod**



More examples can be found in https://github.com/uber/horovod/blob/master/examples/



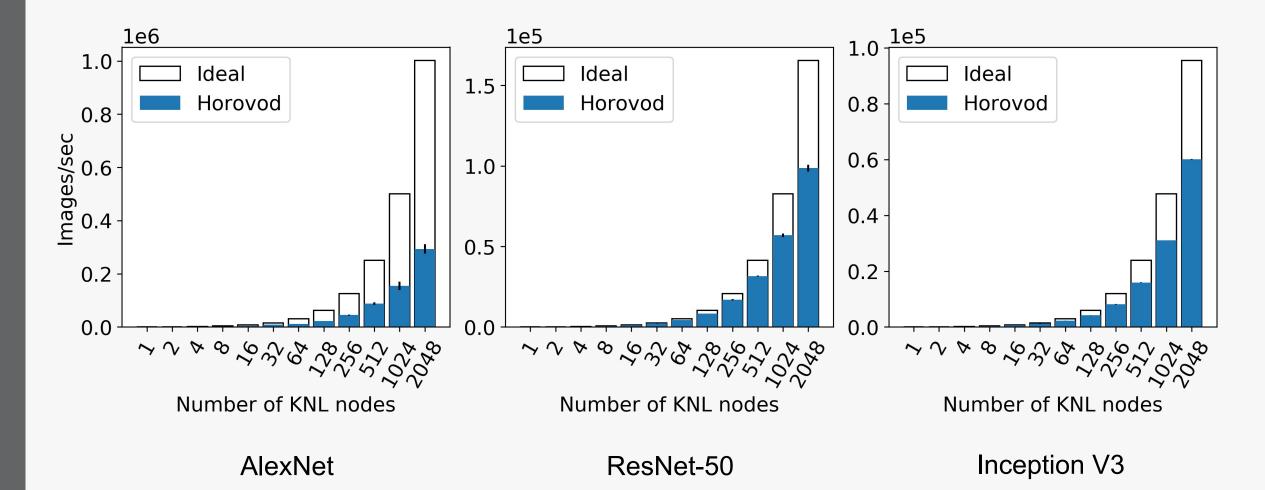
## **Keras with Horovod**



More examples can be found in https://github.com/uber/horovod/blob/master/examples/

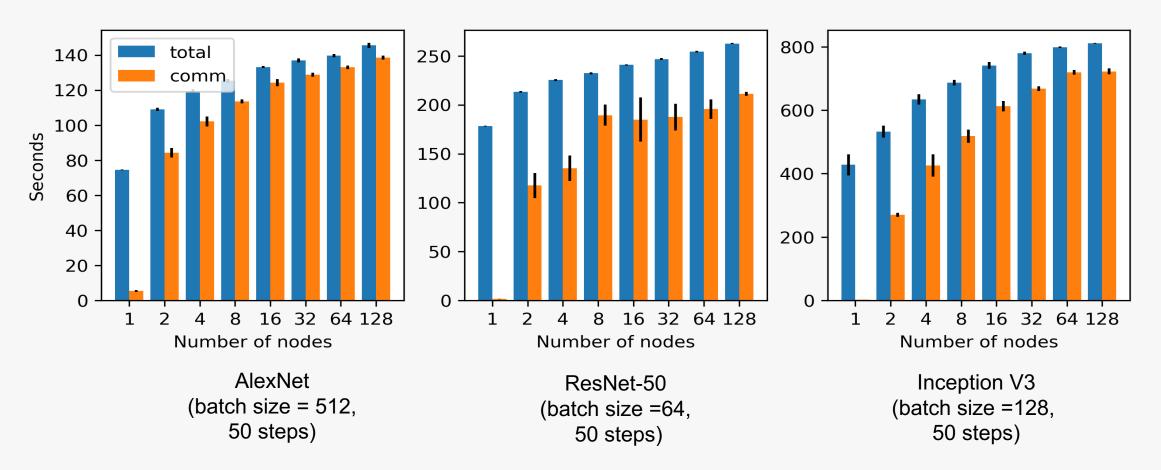


# Scaling TensorFlow using Horovod on Theta @ ALCF (Intel Knights Landing): batch size = 512





## **Overlap of communication and compute in Horovod**



Increase of total time is smaller than the increase of the communication time, which indicates large overlap between compute and communication.



## **MPI flat profile for Horovod**

#### (AlexNet, batch size=512, 128 KNL nodes)

Times and statistics from	MPI_Init() to MPI_	Finalize().		MPI_Allreduce	#calls	avg. bytes	time(sec)
MPI Routine	#calls	avg. bytes	time(sec)		10	4004.0	1.045
					21	16384.0	1.269
MPI_Comm_rank	3	0.0	0.000		10	32768.0	0.521
MPI_Comm_size	3	0.0	0.000		8	1322752.0	0.263
MPI_Bcast	4997	49559.7	1.242		5	3627673.6	0.368
MPI_Allreduce	254	48694759.8	171.666		100	14338464.3	28.882
MPI_Gather	2490	4.0	12.971		50	67108864.0	34.215
MPI_Gatherv	2490	0.0	13.384				
MPI_Allgather	2	4.0	0.001		50	150994944.0	105.104
MPI task 0 of 128 had the	minimum communicat	 ion time.		MPI Gather	#calls	avg. bytes	time(sec)
<pre>synchronization time = total communication time =</pre>	= 42.141 seconds. = 241 404 seconds (	including synch	ronization)	-	2490	4.0	12.971
	= 247.258 seconds.	including synch	10112011011/.				
	= 4618.292 seconds.			MPI_Allgather	#calls	avg. bytes	time(sec)
	= 502.888 seconds.				2	4.0	0.001
-	= 4765.250 MBytes.						
Rank 24 reported the large Rank 117 reported the larg	-		Bytes				

- Majority of time is spent on MPI\_Allreduce with message size ranging from KB-GB
- There is load imbalance (synchronization time)

LD\_PRELOAD=/soft/perftools/hpctw/lib/libmpitrace.so mpirun -np 8 python 03\_keras\_cnn\_concise\_hvd.py --epochs 10



#### **Horovod Timeline**

HOROVOD\_TIMELINE=gpu.json mpirun -np 8 python 03\_keras\_cnn\_concise\_hvd.py HOROVOD\_TIMELINE=cpu.json mpirun -np 8 python 03\_keras\_cnn\_concise\_hvd.py --device cpu

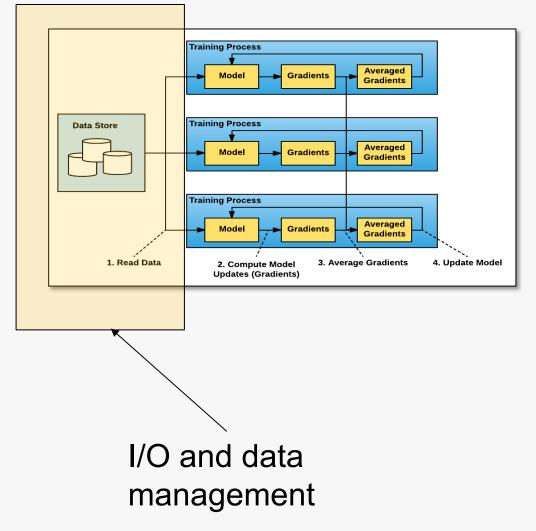
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Record Save Load cpu.json			Flow events Pro	cesses View Option
✓ PartitionedCall/DistributedAdam Allreduce/cond 7/then/ 81	4,000 ms	-duce leand 7/	4,200 ms	4,400 ms
PartitionedCail/DistributedAdam_Ailreduce/cond_//then/_81	/DistributedAdam_Alin	educe/cond_//	HorovodAllreduce_grads	_7_0 (pld 2)
PartitionedCall/DistributedAdam_Allreduce/cond_6/then/_73	/DistributedAdam_Allr	educe/cond_6/	HorovodAllreduce_grads	_6_0 (pid 3)
<b>▼</b>				
PartitionedCall/Distri     Adam_Allreduce/cond_4/then/_57     Adam_Allreduce/cond_4/then/_57		educe/cond_4/	HorovodAllreduce_grads	_4_0 (pid 4)
		I <mark>PI</mark>	MP	MPI
✓ PartitionedCall/Distri Adam Allreduce/cond 3/then/ 49	/DistributedAdam_Allro	educe/cond_3/	HorovodAllreduce_grads	3_0 (pid 5)
✓				
PartitionedCall/DistricucalAdam_Allreduce/cond_1/then/_33	/DistributedAdam_Allro	educe/cond_1/	HorovodAllreduce_grads	_1_0 (pid 6)
<b>▼</b>		N	N	
<ul> <li>PartitionedCall/DistributedAdam Allreduce/cond/then/ 25/D</li> </ul>	istributedAdam Allredu	uce/cond/Horo	odAllreduce grads 0 (n	id 7)
76 items selected. Slices (76)				
Name 🗢	Wall Duration 🔻	Self time $ \bigtriangledown $	Average Wall Duration	$\bigtriangledown$ Occurrences $\bigtriangledown$
NEGOTIATE_ALLREDUCE	134.906 ms	134.906 ms	6.745	ms 20
ALLREDUCE Q	133.568 ms	9.951 ms	6.678	ms 20
MPI_ALLREDUCE	123.312 ms	123.312 ms	6.166	ms 20
MEMCPY_OUT_FUSION_BUFFER	0.157 ms	0.157 ms	0.020	ms 8
MEMCPY IN FUSION BUFFER	0.148 ms	0.148 ms	0.019	ms 8
Totals	392.091 ms	268.474 ms	5.159	ms 76
Selection start				3.877.523 ms
Selection start				625.228 ms
				0201220 111

Record Save Load gpu.json			Flow events Process	ses View Optic
	6,110 ms		6,120 ms	
PartitionedCall/DistributedAdam_Allreduce/cond_7	//tnen/_81/DistributedAdam_Alir NEG ALL	NEG A	NEG A	NEG A
	WAI	WAI	WAI	WAI
PartitionedCall/DistributedAdam_Allreduce/cond_6	6/then/ 73/DistributedAdam Allr	educe/cond 6/	HorovodAllreduce grads 6 (	) (pid 2)
	NEG ALL	NEG A	NEG A	NEG A
	WAI	WAI	WAI	WAI
PartitionedCall/Distri K Adam_Allreduce/cond_4	4/then/_57/DistributedAdam_Allr	educe/cond_4/I	HorovodAllreduce_grads_4_0	) (pid 3)
	NEG ALL	NEG A	NEG A	NEG A
-	WAI	WAI	WAI	WAI
PartitionedCall/Distri Adam_Allreduce/cond_	5/then/_65/DistributedAdam_Allr			
	NEG A	NEG A	NEG A	NEG A
	WA	WAI	WAI	WAI
PartitionedCall/DistributerAdam_Allreduce/cond_2	2/then/_41/DistributedAdam_Allr	educe/cond_2/l	HorovodAllreduce_grads_2_0	) (pid 5)
•	NEG A	NEG A	A	A
	WA	WAI	WAI	WAI
PartitionedCall/DistributedAdam_Allreduce/cond_3	WA	WAI	WAI	WAI
PartitionedCall/DistributedAdam_Allreduce/cond_3	WA	WAI	WAI	WAI
160 items selected.   Slices (160)	WA 3/then/_49/DistributedAdam_Allr	WAI educe/cond_3/I	WAI	VAI D (pid 6)
160 items selected. Slices (160) Name マ	WA 3/then/_49/DistributedAdam_Allr	WAI educe/cond_3/I Self time マ	WAI HorovodAllreduce_grads_3_(	(pid 6) Occurrences
160 items selected. Slices (160) Name マ ALLREDUCE ♀	3/then/_49/DistributedAdam_Allr Wall Duration 👻	WAI educe/cond_3/I Self time マ 2.273 ms	WAI HorovodAllreduce_grads_3_( Average Wall Duration ~	0 (pid 6) Occurrences 20
160 items selected. Slices (160) Name マ ALLREDUCE ۹ NEGOTIATE_ALLREDUCE ۹	WA 3/then/_49/DistributedAdam_Allr Wall Duration ▼ 25.513 ms	WAI           educe/cond_3/I           Self time ♥           2.273 ms           19.053 ms	WAI HorovodAllreduce_grads_3_( Average Wall Duration マ 1.276 ms	VAI 0 (pid 6) Occurrences 20 20
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160 items selected. Slices (160) Name ▼ ALLREDUCE NEGOTIATE ALLREDUCE WAIT FOR OTHER TENSOR DATA WAIT FOR DATA NCCL ALLREDUCE MEMCPY_OUT_FUSION_BUFFER	WA           3/then/_49/DistributedAdam_Allr           Wall Duration ▼           25.513 ms           19.053 ms           15.478 ms           3.897 ms           2.985 ms           0.528 ms	WAI           educe/cond_3/l           Self time           2.273 ms           19.053 ms           15.478 ms           3.897 ms           2.985 ms           0.528 ms           0.178 ms	WAI HorovodAllreduce_grads_3_( Average Wall Duration マ 1.276 ms 0.953 ms 0.774 ms 0.195 ms 0.149 ms 0.026 ms	O (pid 6)           O (courrences)           20



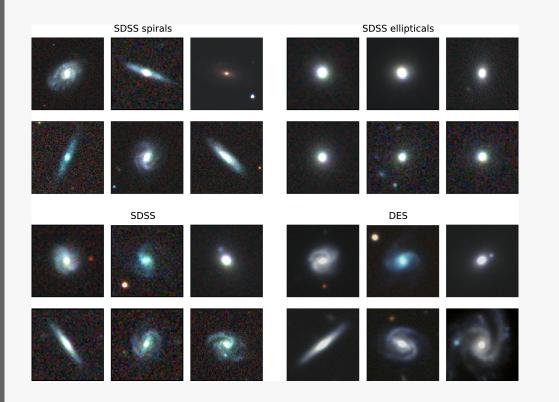
## I/O and data management

- Parallel IO is needed: each worker only reads part of the dataset they needed(using MPIIO / parallel HDF5)
- Preprocess the raw data (resize, interpolation, etc) into binary format before the training. *Shuffling in the memory instead of in I/O*
- Store the dataset in a reasonable way (avoiding file per sample)
- Prefetch the data (from disk; from host to device) Streaming I/O provided by frameworks

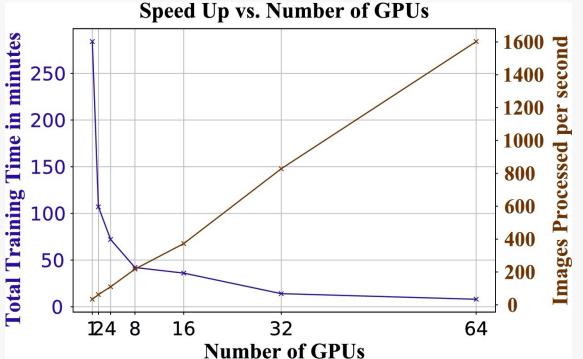




# Science use case 1 - Galaxy classification using modified Xception model



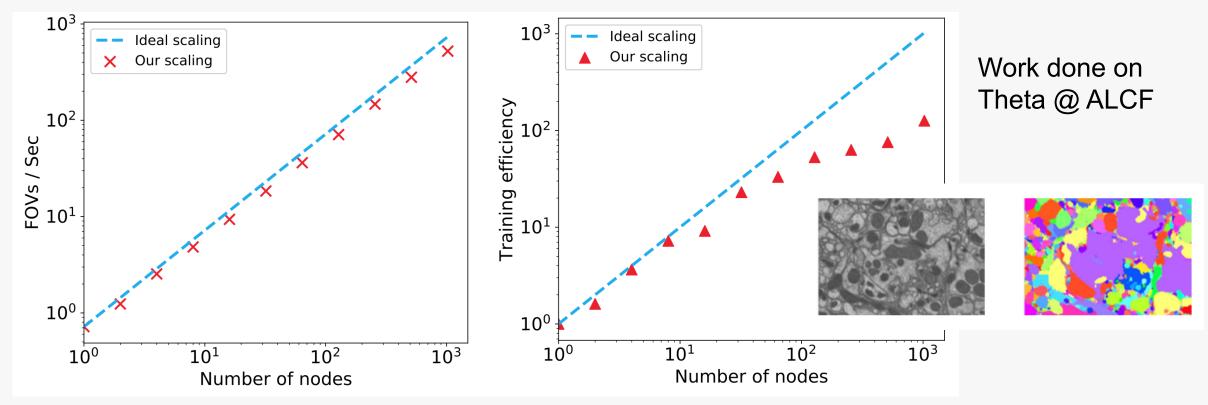
Galaxy images



~ 5 Hrs using 1 K80 GPU to 8 mins using 64 K80 GPUs using computing resource from Cooley @ ALCF



# Science use case 2 - Brain Mapping: reconstruction of brain cells from volume electron microscopy data



Scaling results in terms of throughput

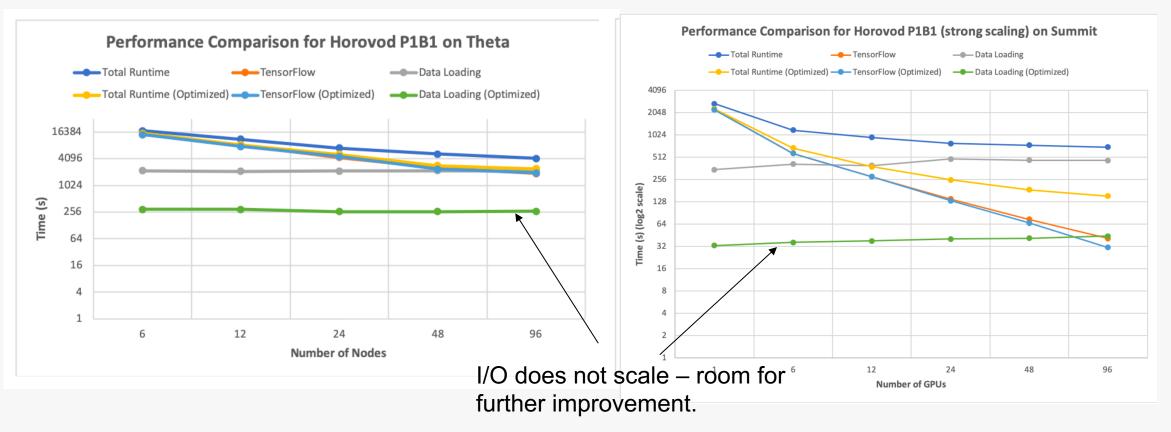
Scaling results in terms of training efficiency (measured by time needed for the training to reach to certain accuracy)

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W. Dong et al, arXiv:1905.06236 [cs.DC]



# Science use case 3 - CANDLE benchmarks: deep learning for cancer problems



#### Strong scaling study of CANDLE P1B1 on Theta and Summit

X. Wu et al SC18 Workshop on Python for High-Performance and Scientific Computing





#### Conclusion

• Increase of model complexity and the amount of dataset

• Data parallelism can scale efficiently in HPC supercomputers

• Warm up steps might be needed to stabilize the initial stage of training and to avoid the generation gap for large batch size training

• Distributed learning requires efficient and scalable I/O and data management.





#### References

- https://horovod.readthedocs.io/en/stable/
- Sergeev, A., Del Balso, M. (2017) Meet Horovod: Uber's Open Source Distributed Deep Learning Framework for TensorFlow. Retrieved from <u>https://eng.uber.com/horovod/</u>
- Sergeev, A. (2017) Horovod Distributed TensorFlow Made Easy. Retrieved from https://www.slideshare.net/AlexanderSergeev4/horovod-distributed-tensorflow-made-easy





#### **Hands on Exercise**

ssh <username><u>@polaris</u>.alcf.anl.gov

ssh <username>@ <u>theta</u>.alcf.anl.gov ssh **thetagpusn1** 

module load datascience

/lus/grand/projects/ATPESC2022/EXAMPLES/track-8-ML/Horovod\_Examples\_atpesc22 [Polaris] /grand/projects/ATPESC2022/EXAMPLES/track-8-ML [Theta]

**cd** /lus//grand/projects/ATPESC2022/usr/<username> [Polaris] cd /grand/projects/ATPESC2022/usr/<username> [Theta]

qsub -I select=16:system=polaris -I walltime=01:00:00 -A ATPESC2022 -q R313446 ./qsub\_polaris.sc qsub -A ATPESC2022 -q ATPESC2022 -n 16 -t 60 --attrs filesystems='home,grand,theta-fs0' ./qsub\_theta.sc qsub -A ATPESC2022 -q training-gpu -n 16 -t 60 --attrs filesystems='home,grand,theta-fs0' ./qsub\_thetagpu.sc

- 1. https://status.alcf.anl.gov/theta/activity
- 2. https://github.com/argonne-lcf/ATPESC\_MachineLearning
- 3. <u>https://github.com/argonne-lcf/sdl\_ai\_workshop 4</u>.
- 4. <u>https://github.com/argonne-lcf/ai-science-training-series</u>
  - 30 Argonne Leadership Computing Facility



# Thank you!

