

Linaro Tools

Rudy Shand Linaro Ltd





HPC development Solutions from Linaro

Build reliable and optimized code on multiple Server and HPC architectures

Linaro Forge combines



Linaro DDT

Market leading, simple to use HPC debugger for C/C++, Fortran and Python applications.



Linaro MAP

Effortless performance analysis for experts and novices alike.



LinaroPerformance Reports

At a glance, single-page, application performance summary.

Performance
Engineering for any
architecture, at any
scale





gdb under the hood

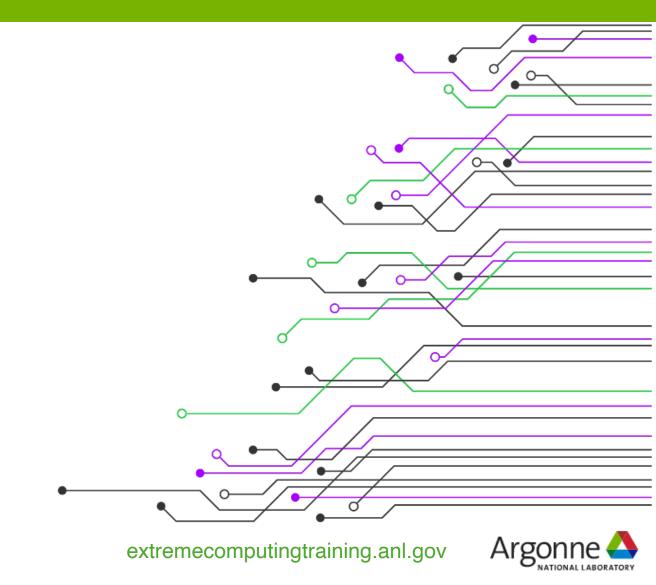
Leveraging the gdb community

GDB is the underlying technology

- Linaro upstreams patches to the gdb community
- Forge team raises and fixes gdb bugs

Nimble at supporting new technologies

- Rely on hardware vendors to add gdb support
- Rely on software consortiums to add gdb support
- Helps us to stay current with technologies
- Provides a state of the art debugger

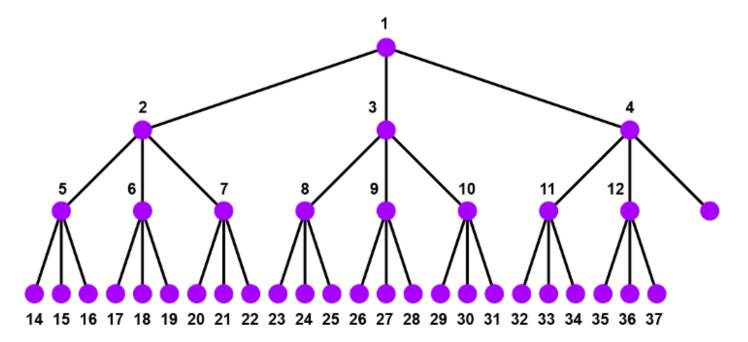




Scaling gdb

Tree network topology

- Tree server design is how Forge is able to scale
- Send bulk commands and merge responses
- Aggregate the data instead of broadcasting thousands of responses

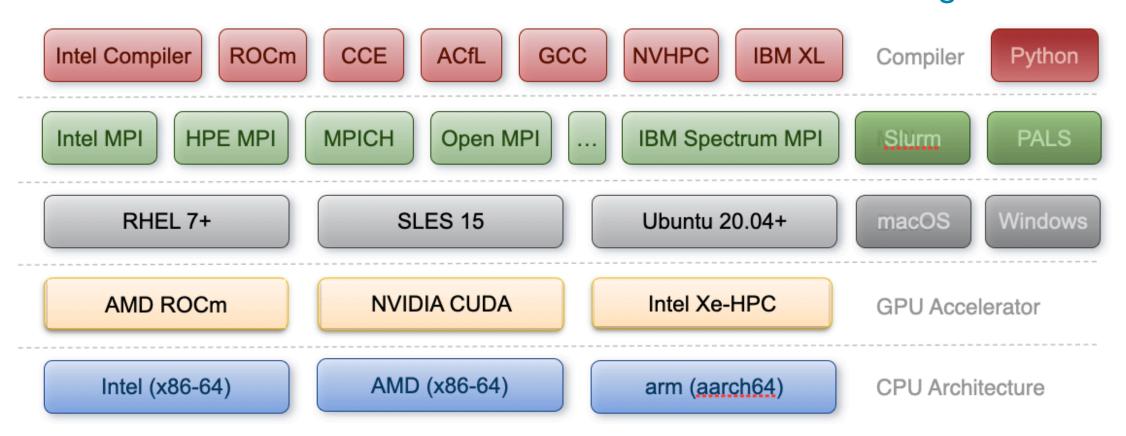






DDT Supported platforms

Works across hardware architectures and HPC technologies

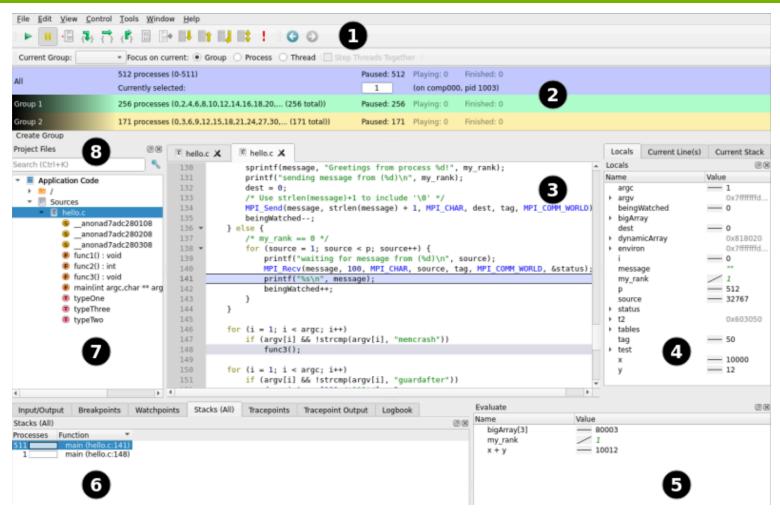






DDT UI

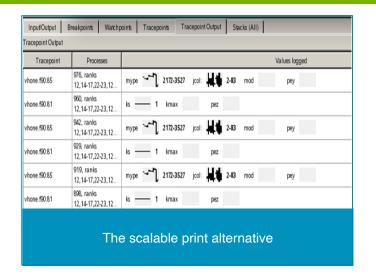
- 1 Process controls
- 2 Process groups
- 3 Source Code view
- 4 Variables
- 5 Evaluate window
- 6 Parallel Stack
- 7 Project files
- 8 Find a file or function

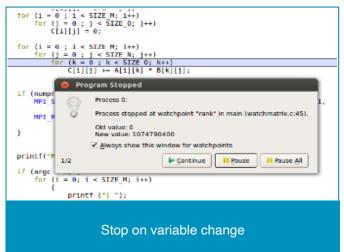


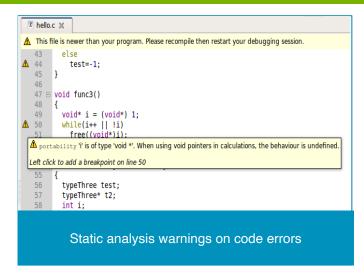




DDT Highlights



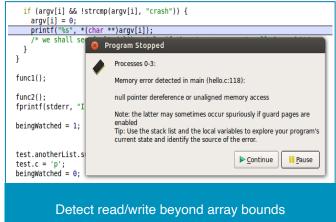


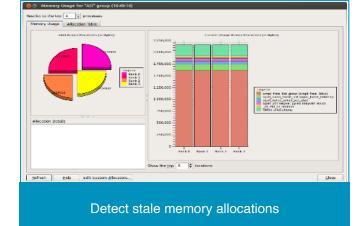


Memory Debugging

```
# Enable reading of debug environment variables:
host:~/demo> export NEOReadDebugKeys=1
```

```
# Disable RTLD_DEEPBIND, so that malloc/free calls bind to
# our memory debug library, instead of glibc:
host:~/demo> export NEODisableDeepBind=1
```

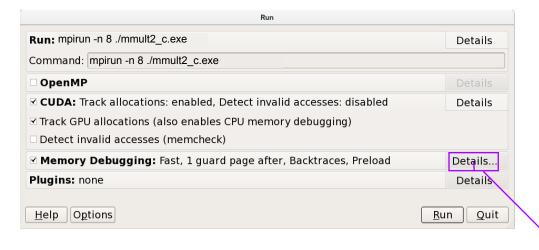






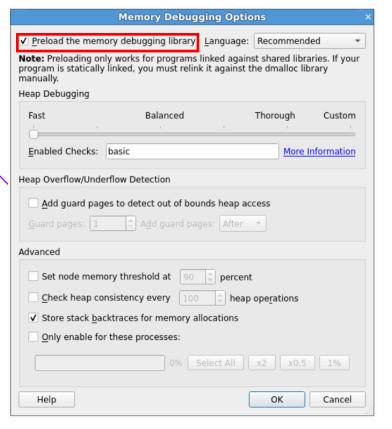


Memory Debugging





When manual linking is used, untick "Preload" box







MDA Viewer

What does your data look like at runtime?

View arrays

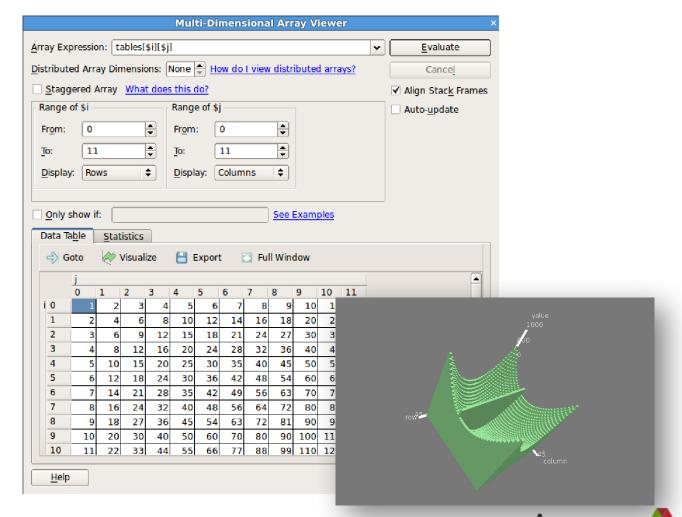
- On a single process
- Or distributed on many ranks

Use metavariables to browse the array

- Example: \$i and \$j
- Metavariables are unrelated to the variables in your program
- The bounds to view can be specified
- Visualise draws a 3D representation of the array

Data can also be filtered

 "Only show if": \$value>0 for example \$value being a specific element of the array





Terminology

	NVIDIA GPU	AMD GPU	Intel GPU
Name	CUDA	ROCm	Xe
Language	CUDA C/C++ and Fortran	HIP	SYCL
Execution Unit	Warp	Wavefront	Sub-group
EU Size	32 Threads	64 Threads	8, 16 or 32 Threads
EU GDB		GDB Thread	GDB Thread
EU Thread		Lane	Lane (work-item)
Forge GPU Thread	Lane	Lane	Lane





Debugging Intel Xe GPUs

Using Linaro DDT

Debug code simultaneously on the GPU and the CPU

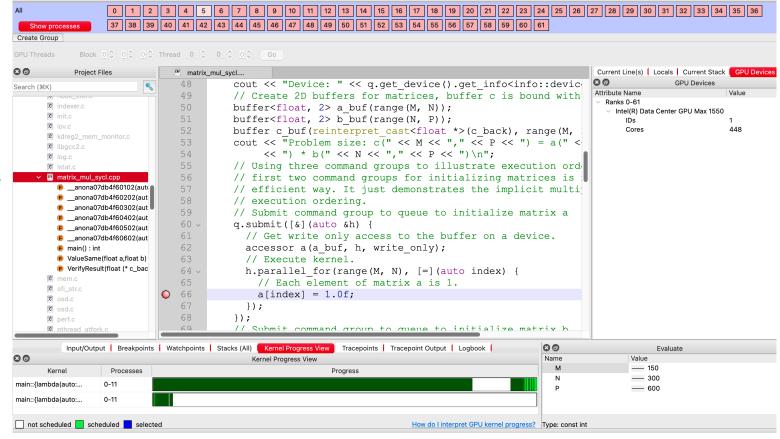
Controlling the GPU execution:

- All active threads in a Sub-group will execute in lockstep. Therefore, DDT will step16 threads at a time.
- Play/Continue runs all GPU threads
- Pause will pause a running kernel

Key (additional) GPU features:

- Kernel Progress View
- GPU thread in parallel stack view
- GPU Thread Selector
- GPU Device Pane

Kernels must be compiled with the -g and -O0 flags







Debugging Nvidia GPUs

Using Linaro DDT

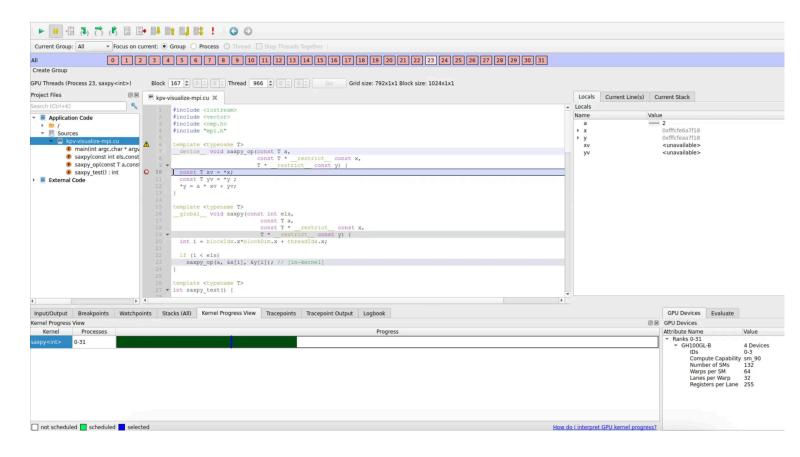
Controlling the GPU execution:

- All active threads in a warp will execute in lockstep Therefore, DDT will step 32 threads at a time.
- Play/Continue runs all GPU threads
- Pause will pause a running kernel

Key (additional) GPU features:

- Kernel Progress View
- GPU thread in parallel stack view
- GPU Thread Selector
- GPU Device Pane

For NVIDIA's nvcc compiler, kernels must be compiled with the -g and -G flags

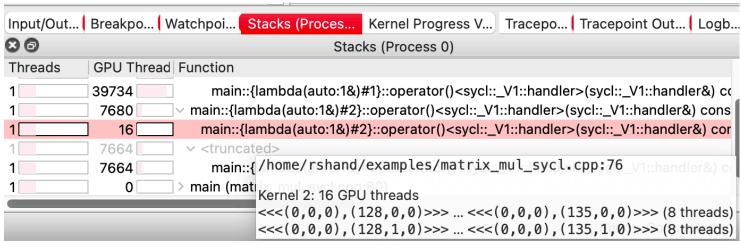






Parallel Stack view

Display location and number of threads



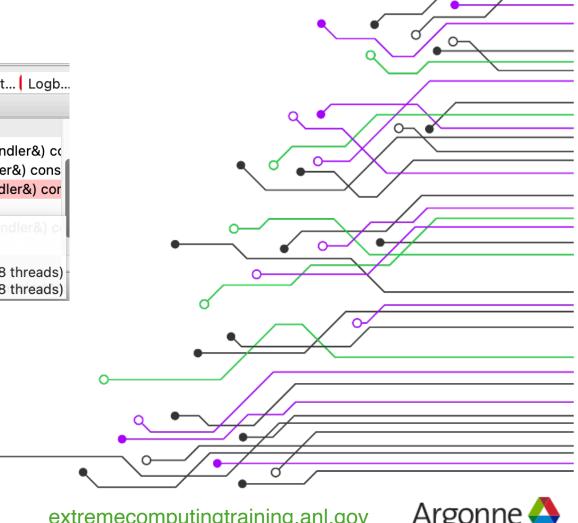
Click stack item

- Select GPU Thread
- Update variable display
- Move Source Code Viewer

Tooltip displays

- GPU Thread Ranges
- Size of each range





Python Debugging

Debug Features

- Sparklines for Python variables
- Tracepoints
- MDA viewer
- Mixed language support

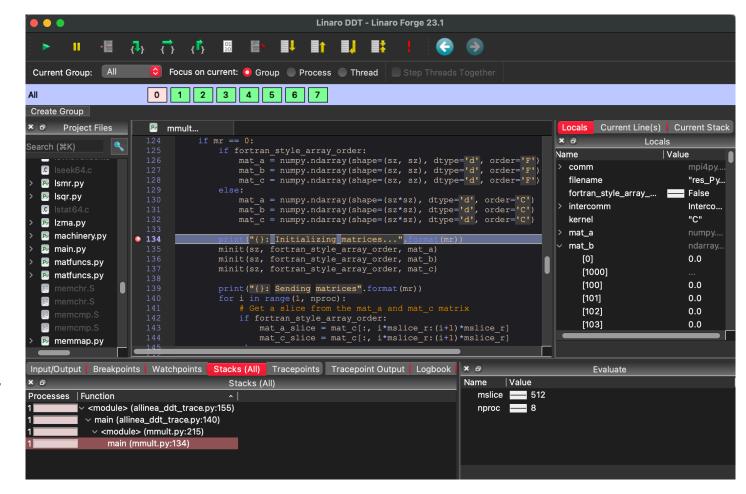
Improved Evaluations:

- Matrix objects
- Array objects
- Pandas DataFrame
- · Series objects

Python Specific:

- Stop on uncaught Python exception
- Show F-string variables in "Current Line" display
- Mpi4py, NumPy, SciPy

ddt --connect mpirun -n 8 python3 %allinea_python_debug% ./mmult.py







DDT in offline mode

Run the application under DDT and halt or report when a failure occurs

You can run the debugger in non-interactive mode

- For long-running jobs / debugging at very high scale
- For automated testing, continuous integration...

To do so, use following arguments:

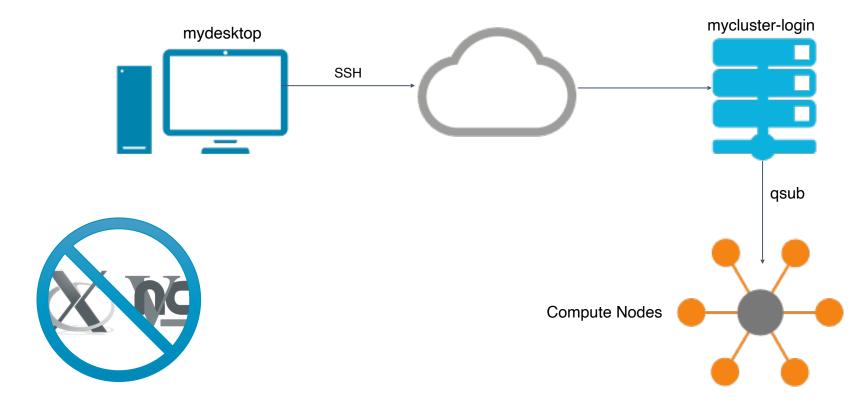
- \$ ddt --offline --output=report.html mpirun ./jacobi_omp_mpi_gnu.exe
 - --offline enable non-interactive debugging
 - --output specifies the name and output of the non-interactive debugging session (HTML or Txt)
 - Add --mem-debug to enable memory debugging and memory leak detection





The Forge GUI and where to run it

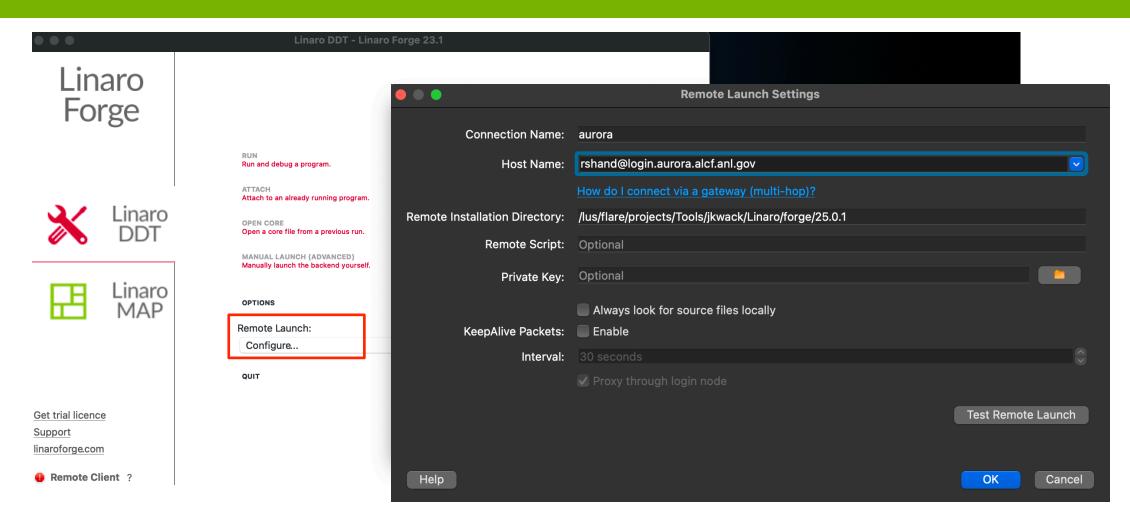
Forge provides a powerful GUIs that can be run in a variety of configurations







Remote connection to Aurora







Cheat sheet

Training material

- 1. Getting the examples cp /flare/ATPESC2025/EXAMPLES/track6-tools/linaro-forge/linaro-forge-training.tar.gz tar -xf linaro-forge-training.tar.gz
- 2. Set the path to the forge training folder export FORGE_TRAINING=<path_to_training_folder>

Forge Client (On local machine)

Install Forge client https://www.linaroforge.com/downloadForge

Running with a batch script

qsub \$FORGE_TRAINING/submit-polaris.sh

Interactive Session

qsub -I -l select=1 -l filesystems=home:flare -l walltime=1:00:00 -q ATPESC -A ATPESC2025

Forge commands

ddt --connect # Reverse connect

ddt --offline # Run DDT without GUI

map --profile # Profile without GUI

perf-report # Generate Performance Report

Guides

Forge userguide

Debugging on Aurora





Debugging with DDT on Aurora

- 1. build deadlock, simple, memory_debugging and split examples cd \$FORGE_TRAINING/correctness/debug make
- 2. Get an interactive session
- 3. export FORGE_LICENSE_FILE=/pe/licenses/arm_forge/Licence
- 4. split
 //us/flare/projects/Tools/jkwack/Linaro/forge/25.0.1/bin/ddt --np=12 --connect --mpi=generic --mpiargs='--ppn 12 --envall' ./split
- 6. Memory_debugging
 //us/flare/projects/Tools/jkwack/Linaro/forge/25.0.1/bin/ddt --np=1 --connect --mpi=generic --mpiargs='--ppn 1 --envall' ./memory_debugging
 //us/flare/projects/Tools/jkwack/Linaro/forge/25.0.1/bin/ddt --np=4 --connect --mpi=generic --mpiargs='--ppn 4 --envall' ./memory_debugging





Debugging Intel Xe GPUs using DDT on Aurora

```
cd $FORGE_TRAINING/correctness/gpu-intel-mmult

mpicxx -fsycl -g -00 matrix_mul_sycl.cpp -o matrixmul

qsub -l select=2 -l walltime=1:00:00 -l filesystems=home:flare -A ATPESC2025 -q ATPESC -I

./soft/compilers/oneapi/2025.1.0/debugger/2025.1/env/vars.sh

https://docs.alcf.anl.gov/aurora/debugging/ddt-aurora/#invoking-the-ddt-server-from-aurora

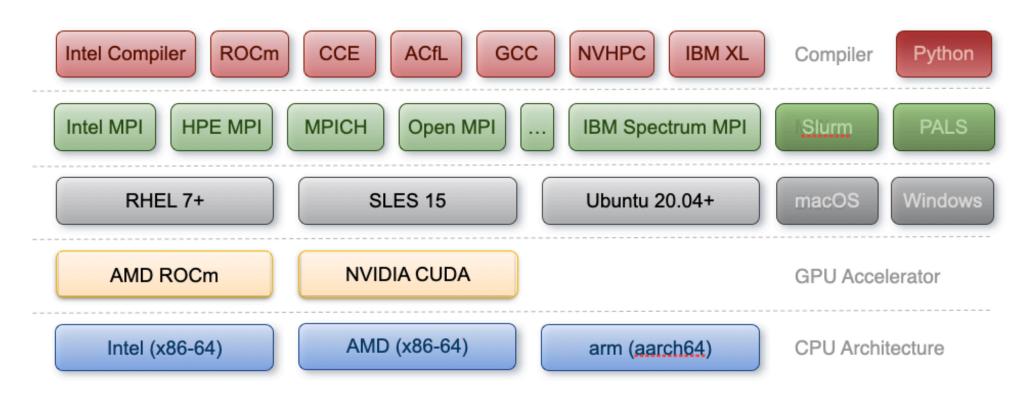
ddt --np=24 --connect --mpi=generic --mpiargs="--ppn 12 --envall" ./matrixmul
```





MAP Supported platforms

Works across hardware architectures and HPC technologies







Linaro Performance Reports

Characterize and understand the performance of HPC application runs



Gather a rich set of data

- Analyses metric around CPU, memory, IO, hardware counters, etc.
- Possibility for users to add their own metrics



Build a culture of application performance & efficiency awareness

- Analyses data and reports the information that matters to users
- Provides simple guidance to help improve workloads' efficiency



Adds value to typical users' workflows

- Define application behaviour and performance expectations
- Integrate outputs to various systems for validation (eg. continuous integration)
- Can be automated completely (no user intervention)





Linaro Performance Reports

A high-level view of application performance with "plain English" insights

arm

Command:

mpiexec.hydra -host node-1,node-2 -map-by socket -n 16 -ppn 8 ./Bin/low_freq/../../Src//hydro

-i

PERFORMANCE REPORTS ./Bin/low_freq/../../../Input/input_250x125_corner.nml

Resources: 2 nodes (8 physical, 8 logical cores per node)

Memory: 15 GiB per node

Tasks: 16 processes, OMP_NUM_THREADS was 1

Machine: node-1

Start time: Thu Jul 9 2015 10:32:13

Total time: 165 seconds (about 3 minutes)

Full path: Bin/../Src

1/0

A breakdown of the 16.2% I/O time:

Time in reads 0.0%

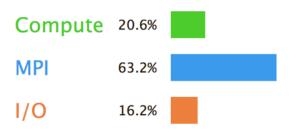
Time in writes 100.0%

Effective process read rate 0.00 bytes/s

Effective process write rate 1.38 MB/s

Most of the time is spent in write operations with a very low effective transfer rate. This may be caused by contention for the filesystem or inefficient access patterns. Use an I/O profiler to investigate which write calls are affected.

Summary: hydro is MPI-bound in this configuration



 $\label{thm:code} \mbox{Time spent running application code. High values are usually good.}$

This is very low; focus on improving MPI or I/O performance first

Time spent in MPI calls. High values are usually bad.

This is high; check the MPI breakdown for advice on reducing it

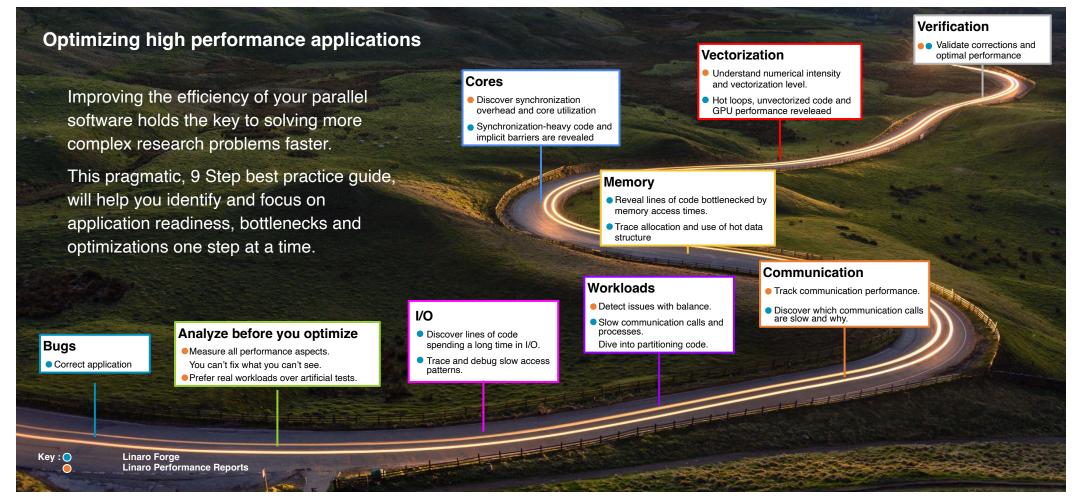
Time spent in filesystem I/O. High values are usually bad.

This is average; check the I/O breakdown section for optimization advice





Performance Roadmap

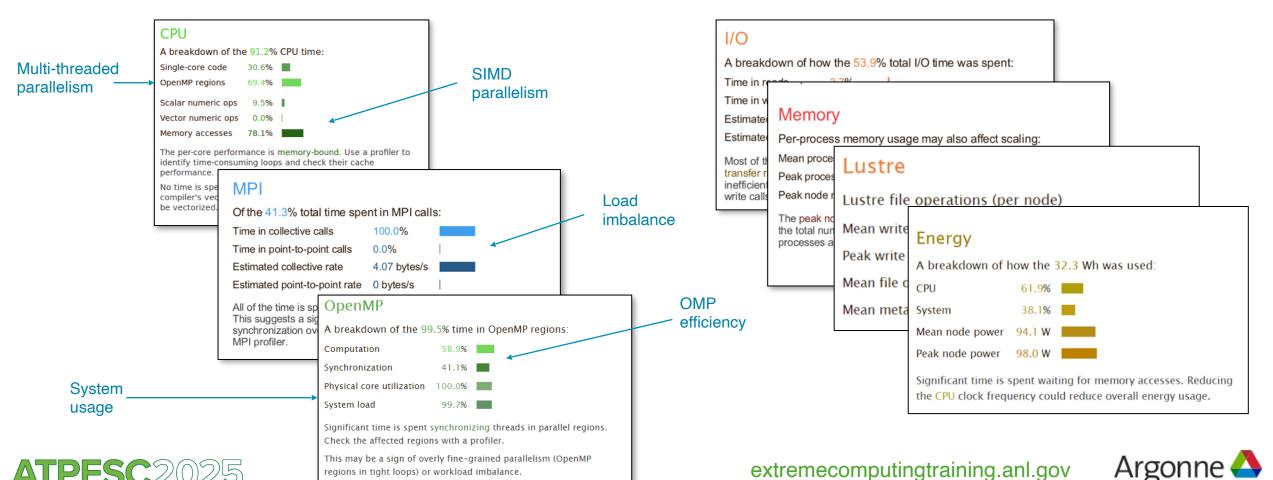






Linaro Performance Reports Metrics

Lowers expertise requirements by explaining everything in detail right in the report



MAP Capabilities

MAP is a sampling based scalable profiler

- Built on same framework as DDT
- Parallel support for MPI, OpenMP, CUDA
- Designed for C/C++/Fortran

Designed for 'hot-spot' analysis

- Stack traces
- Augmented with performance metrics

Adaptive sampling rate

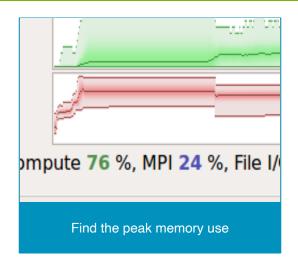
- Throws data away 1,000 samples per process
- Low overhead, scalable and small file size

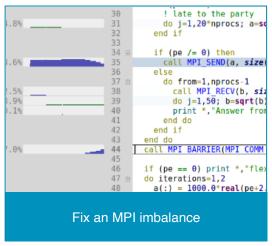


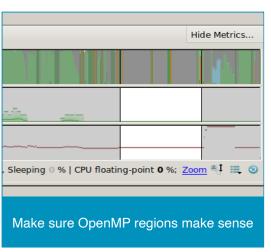


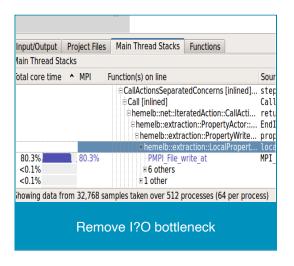


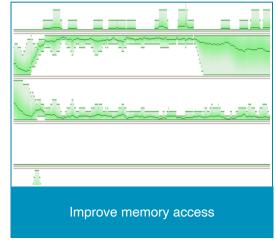
MAP Highlights

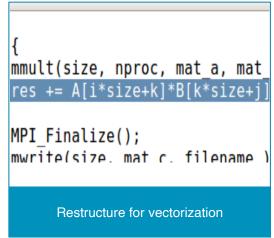








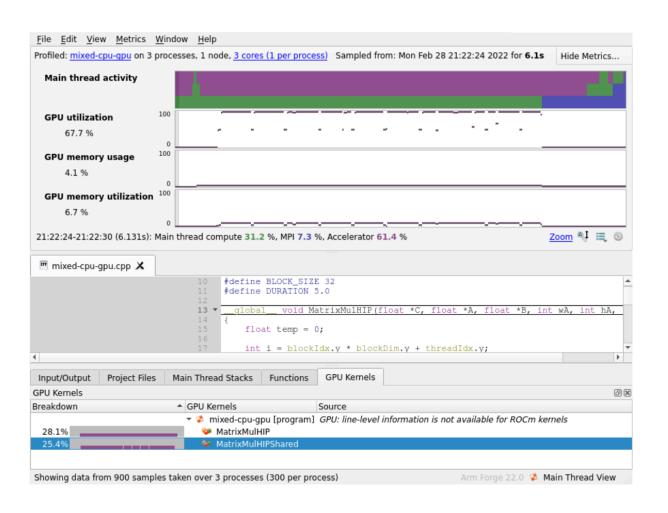








GPU Profiling



Profile

- Supports both AMD and Nvidia GPUs
- Able to bring up metadata of the profile
- Mixed CPU [green] / GPU [purple] application
- CPU time waiting for GPU Kernels [purple]
- GPU Kernels graph indicating Kernel activity

GUI information

- GUI is consistent across platforms
- Zoom into main thread activity
- Ranked by highest contributors to app time





Python Profiling

19.0 adds support for Python

- Call stacks
- Time in interpreter

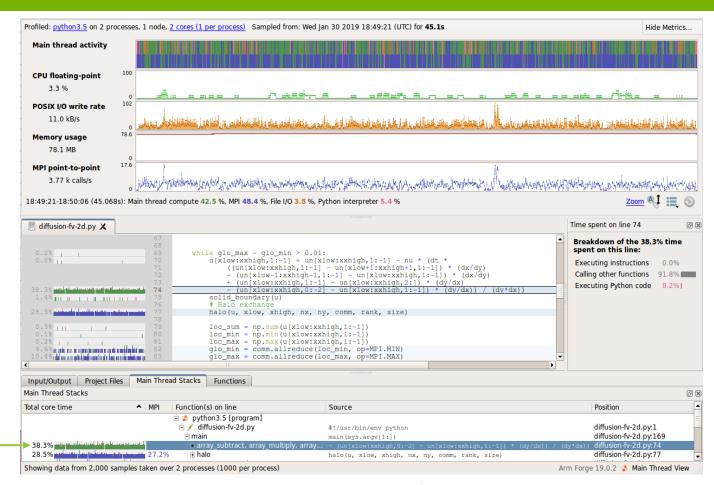
Works with MPI4PY

Usual MAP metrics

Source code view

Mixed language support

Note: Green as operation is on numpy array, so backed by C routine, not — Python (which would be pink)



map --profile mpirun -n 2 python ./diffusion-fv-2d.py





Compiler Remarks

Annotates source code with compiler remarks

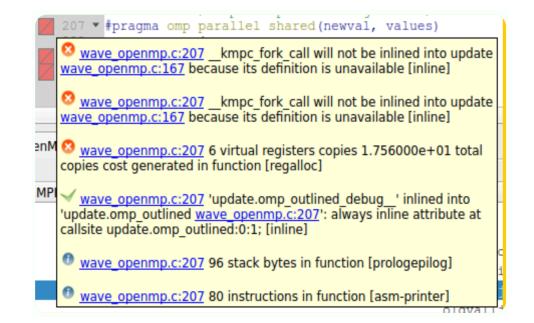
- Remarks are extracted from the compiler optimisation report
- Compiler remarks are displayed as annotations next to your source code

Colour coded

- Their colour indicates the type of remark present in the following priority order:
- 1. Red: failed or missed optimisations
- 2. Green: successful or passed optimisations
- 3. White: information or analysis notes

Compiler Remarks menu.

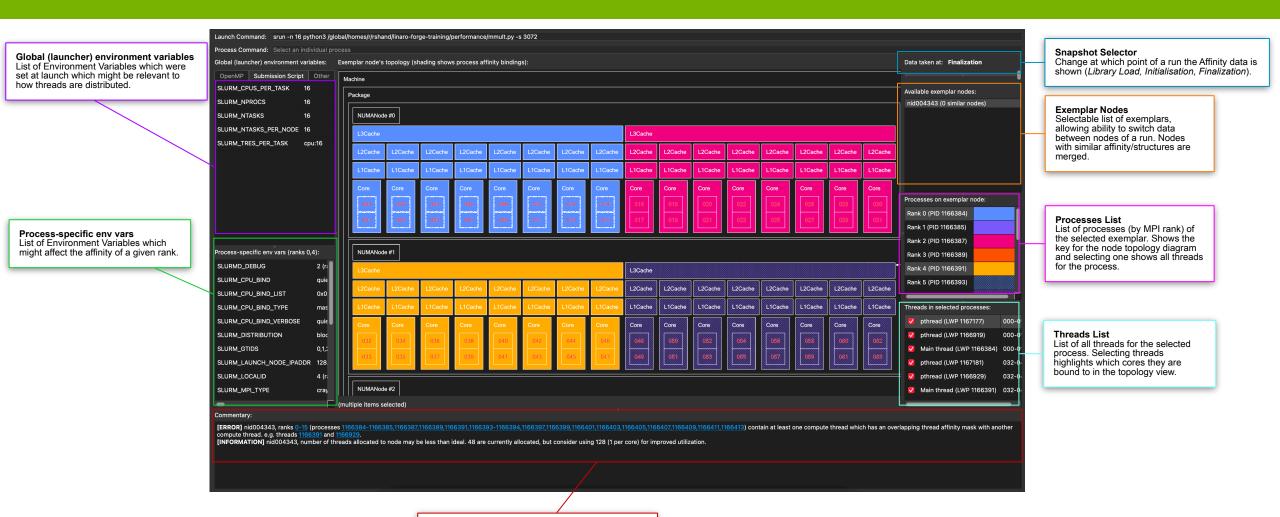
- Specify build directories for non-trivial build systems
- Filter out remarks







MAP Thread Affinity Advisor



A list of commentary, providing information and

advice on Memory Imbalance, Core Utilization etc.



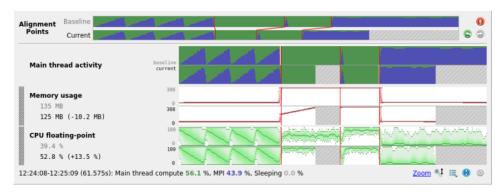
extremecomputingtraining.anl.gov

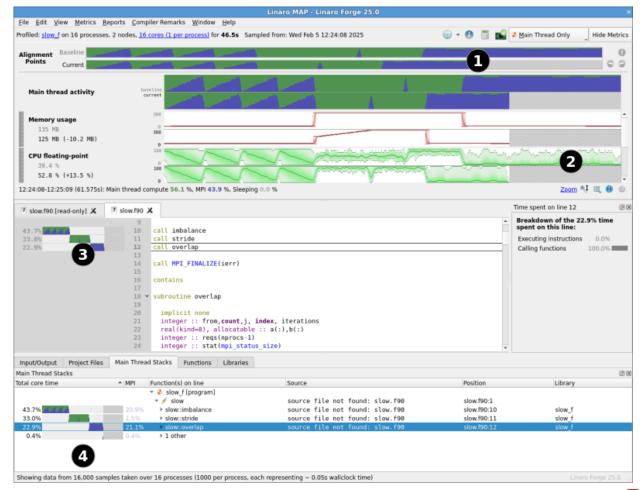


Differences between two profiles

MAP Diff (--baseline support)

- MAP Diff allows comparisons of two MAP profiles, useful for identifying performance changes between different parameters, compilers, libraries and systems.
- Use the alignment points view (1) to line up phases of execution.
- Compare metric graphs of the two profiles (2), including metric summaries for each.
- See gaps in activity in the source code viewer (3) and stacks views (4), including OpenMP Regions View, Functions View, Library view and GPU Kernels/Memory Transfer View.









Profiling with MAP on Polaris

Worked Example: https://docs.linaroforge.com/23.1.1/html/forge/worked_examples_appendix/mmult/analyze.html

1. Setup the environment

qsub -I -I select=1 -I filesystems=home:eagle -I walltime=0:30:00 -q ATPESC -A ATPESC2025 module use /soft/modulefiles module load forge cray-cti module load conda/2024-04-29 conda activate base

- 2. Build the Python example cd \$FORGE_TRAINING/performance make -f mmult_py.makefile
- 3. Run the Python example

 MPICH_GPU_SUPPORT_ENABLED=0 map --profile mpiexec -n 8 python ./mmult.py -s 3072
- 4. Offline profile qsub \$FORGE_TRAINING/scripts/submit-polaris.sh





ARGONNE TRAINING PROGRAM ON EXTREME-SCALE COMPUTING

Produced by Argonne National Laboratory, a U.S. Department of Energy Laboratory managed by UChicagoArgonne, LLC under contract DE-AC02-06CH11357.

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